White Papers by Product

White Papers

XC9500 CPLDs

Listed below are the XC9500 CPLD White Papers listed by the most recent paper. Click on the White Paper title to review the document in PDF format.

Number **White Paper Description Product**

WP138 Voice-Data Convergence—Voice Over IP (v1.0) 03/21/01 (359 KB)

This paper gives an overview of voice-data convergence technologies and how Xilinx high volume programmable devices can be used to overcome some of the significant challenges facing the designers of these systems. The Xilinx products targeted at these high-volume applications include XC9500XL™ and CoolRunner™ CPLDs and Spartan™-II FPGAs. This appendix starts with an overview of voice-data convergence technologies and the benefits they bring to the users. We will then describe the product architectures that are used to implement VoIP gateways and IP phones. The final topic will be to show how Spartan-II devices can be used in these applications.

WP120 Xilinx High-Volume Programmable Logic Applications in Satellite Modem Designs (v1.0) 07/21/00 (74 KB)

This paper provides an overview of satellite modem technologies and standards, and discusses how the Internet is driving the deployment of this technology. The Xilinx device families targeted at these high volume applications include XC9500 CPLDs and Spartan &#reg; -II FPGAs.

WP100 Xilinx at Work in Set-Top Boxes (v1.0) 03/28/00 (150 KB)

This White Paper gives an overview of different set-top box technologies and how Xilinx high volume programmable devices can be used to implement complex system level glue in a variety of set-top box designs. It concentrates on set-top box technology used to receive television over satellite, cable and terrestrial channels.

WP104 Xilinx High-volume Programmable Logic Applications in Satellite Modem Designs (v1.0) 01/20/00 (315 KB)

This paper gives an overview of satellite modem technologies and how Xilinx high-volume programmable devices can be used to implementing complex system level glue in satellite modem designs. The Xilinx device families targeted at these high-volume applications include XC9500 CPLDs and Spartan™ FPGAs.

WP103 Xilinx High Volume Programmable Logic Applications in Internet Audio Players (v1.0) 01/17/00 (120 KB)

This paper provides an overview of Internet audio technologies and how Xilinx high-volume programmable devices can be used to overcome some of the significant challenges facing the designers of portable players. The Xilinx device families targeted at these high-volume applications include CoolRunner™ CPLDs and Spartan™ FPGAs.

CPLDs Spartan

FPGAs

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CPLDs, Spartan **FPGAs**

CPLDs, Spartan **FPGAs**

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WP124 Xilinx at Work in Digital Modems (v1.0) 5/19/99 This white paper gives an overview of digital modem technologies and how Xilinx high volume programmable devices can be used to implement complex system level glue in digital modem FPGAs WP125 Xilinx at Work in ISDN Modems (v1.0) 5/19/99 CPLDs,

Spartan

FPGAs

This white paper gives an overview of ISDN modem technologies and how Xilinx high volume programmable devices can be used to implement complex system level glue in ISDN modem designs.