



# USB and PCMCIA AllianceCOREs Now Available

The first products in the AllianceCORE™ program are now available:

- Cores for Universal Serial Bus (USB) applications from CAE/Inventra, Inc., and
- Cores for PCMCIA card design from Mobile Media Research, Inc.

The AllianceCORE program is a cooperative effort between Xilinx and independent third-party developers, with the goal of providing a broad selection of industry-standard cores that are optimized and verified for Xilinx programmable logic devices.

## Universal Serial Bus

The USB AllianceCORE products from CAE/Inventra include a full-speed USB function controller, low-speed USB function controller, and three-port USB hub. These cores, targeted for the XC4000E and XC4000EX FPGA architectures and their HardWire™ equivalents, are delivered as XNF netlists (source code in Verilog RTL format also is available). They are fully-compliant to the USB v1.0 specification, and the FPGA implementations were verified in hardware at a USB-IF sponsored interoperability workshop. To facilitate the integration of these cores, CAE/Inventra also offers a USB simulation model and FPGA-based prototyping board, for both hub and function controllers.

## PCMCIA Fax/Modem

Mobile Media Research offers a PCMCIA fax/modem interface macro and a PCMCIA interface library. Fully PCMCIA v2.0 compliant, the fax/modem core is designed for implementation in XC3000 and XC5000 series FPGAs; when used with an external fax/modem chipset, it forms a complete PCMCIA fax/modem card. The PCMCIA library provides the basic building blocks for constructing a custom PC-card interface. Additional

products include an FPGA-based prototyping board with 12 square inches of breadboarding area, a Windows-based CIS (Card Information Structure) generator, and system-level debug software.

These products are available now from their respective vendors. For vendor contact information, please see the AllianceCORE Partners chart below. Additional information about the AllianceCORE program can be found at WebLINX™ ([www.xilinx.com](http://www.xilinx.com)). ♦

*The chart below will join the other Alliance Program charts in the next issue of XCell.*

## AllianceCORE Partners - Feb. 1997

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*USB, PCI*

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*PCI, MPEG-2, ATM, commun.*

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*Microprocessor peripherals, base functions, Xilinx design*

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*PCMCIA, CardBus*

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