Design FPGAs Using Free, Downloadable WebPACK Software

Free WebPACK ISE software tools are now available for the Spartan-II family of FPGAs.

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You can now implement your Xilinx FPGA designs by downloading free WebPACK™ ISE™ (Integrated Synthesis Environment) tools from Xilinx.com. WebPACK ISE software is popular because it provides excellent, free software support for CPLD designs, and it has been extended to include support for the entire Spartan-II™ FPGA family and the Virtex™ XCV300E device. WebPACK ISE software is a complete development system with no expiration date.

You can try your hand at FPGA design without having to make the up-front investment in software. WebPACK ISE software supports VHDL and Verilog™ design entry for FPGAs with dedicated XST™ (Xilinx Synthesis Technology) software. You may also download industry-leading Model Technology™ HDL test vector generation, and Synopsis VSS™ simulation tools to help verify your designs. With reprogrammable FPGA technology, you can easily download designs and try them out in-system, encouraging an iterative design process. WebPACK ISE tools include the JTAG Programmer™ software, which you can use to download designs with a JTAG cable to any develop-ment board, using JTAG cable. Development boards are available from Xilinx and several third-party vendors.

WebPACK ISE software provides a modular set of tools, allowing you to download and install only what you need for your requirements. All WebPACK ISE downloadable software solutions are supported

Device	System Gates	I/Os	Block RAM bits
XC2S15	15,000	86	16K
XC2S30	30,000	132	24K
XC2S50	50,000	176	32K
XC2S100	100,000	196	40K
XC2S150	150,000	260	48K
XC2S200	200,000	284	56K
XCV300E	300,000	316	128K

Table 1 – FPGAs supported by WebPACK ISE software

on Windows 98, NT 4.0, and 2000 plat-forms. You can register to be notified by e-mail when updates are made available to the WebPACK ISE tools.

WebPACK ISE software for FPGAs includes:

• Project Navigator™

• XST VHDL and Verilog synthesis

• HDL testbench generation

• HDL simulation

• User constraints editor

• Timing-driven implementation

• Multipass place-and-route

• Floorplanning

• Static timing analysis

  • Functional and timing simulation

  • Third-party simulation support

  • Error navigation to the Web

  • 1149.1 JTAG programming

  • Bitstream and PROM config-uration.

WebPACK ISE software is based on the award-winning Xilinx Foundation ISE Series™ software package. It is different from Foundation ISE software, howev-er, in that it is focused on CPLDs and Spartan-II FPGAs, is instant-ly accessible, and it's free. Support is pro-vided for all CPLDs and the FPGA devices listed in Table 1.

WebPACK ISE software is the program-mable logic design system that is the easiest to learn, easiest to use, and easiest to get. You will be up and running fast with the Xilinx modular approach to Web-based delivery. See for yourself. Download the tools you need today from www.xilinx.com/sxpresso/webpack.htm.