

Xilinx Announces Terabit Networking Forum

Industry leaders to discuss next generation system interconnect technologies that meet the network demands of the terabit revolution.

by Ron DiGiuseppe

System Interfaces Product Marketing Manager, Xilinx
ron.digiuseppe@xilinx.com

Xilinx, in collaboration with industry leaders, is hosting the Terabit Networking Forum – an all-day event focusing on the rapidly evolving system interconnect technologies for the networking industry. The event will be held at the Santa Clara Convention Center on August 21, 2001, and will provide a technical overview of current interface technologies as well as insights into the future of interface standards, applications, and silicon solutions. Over 500 networking executives, system architects, design engineers, and technical, product and marketing managers are expected to attend the free event. The forum will highlight leading edge technologies and provide an understanding of the latest trends that will help you improve performance and stay on the forefront of networking and communications technologies. For registration and program information, visit: www.xilinx.com/terabit

SystemIO Interface Standards

Presentations will be made by industry analysts and experts as well as Xilinx technology staff. Topics will include 10 Gigabit Ethernet, HyperTransport, Infiniband, CSIX, POS PHY Level 4 and Level 5, RapidIO and 3GIO interconnect technologies, and more. There will also be live demonstrations of interface standards as well as a panel discussion focusing on the challenges of interface adoption, interoperability, and compliance in an exploding, often undefined marketplace.

This industry impact event will provide an unparalleled opportunity to meet with peers and industry leaders, so you can discuss common opportunities and challenges, and derive your own strategies for success.

Year 2001 Worldwide Xilinx Event Schedules

Year 2001 North American Event Schedule

June 18-20	38th Design Automation Conference	Las Vegas, CA
June 24-27	2001 ASEE Conference & Expo	Albuquerque, NM
June 21-23	WITI Technology Summit 2001	Santa Clara, CA
July 16-20	NSREC 2001	Vancouver, BC
Sept 2001	SNUG 2001 Boston	Boston, MA
Sept 26-28	MAPLD 2001	Johns Hopkins, MD
Oct 1-4	Communication Design Conf. 2001	San Jose, CA
Oct 10-13	Frontiers in Education 2001	Reno, NV
Oct 30	Embedded Computing Show 2001	San Diego, CA

Year 2001 European Event Schedule

June 20-23	ITS 2001	Bilbao, Spain
June 26-28	ADEC 2001	Midrand, South Africa
Aug 26-29	Field Programmable Logic Conf.	Belfast, UK
Sept 10-14	RADECS 2001	Grenoble, France
Oct 9-11	Embedded Systems Conference	Stuttgart, Germany
Oct 15-16	SoC for a Connected World	Lyon, France

Year 2001 Asia Pacific Event Schedule

June 27-28	IIC Expo 2001	Seoul, Korea
July 2-3	IIC Expo 2001	Taipei, Taiwan
Oct 3-4	EDA&T 2001	Hsinchu, Taiwan
Nov 2001	Xilinx Technical Seminars 2001	Asia Pacific

Year 2001 Japanese Event Schedule

June 2001	Xilinx KK Expo 2001	Tokyo and Osaka, Japan
Nov 2001	Xilinx Technical Seminars 2001	Japan and SE Asia
Nov 2001	MST Fair 2001	Tokyo, Japan

For more information about Xilinx Worldwide Events, please contact one of the following Xilinx team members or see our website at: <http://www.xilinx.com/company/events.htm>

- North American Shows: Darby Mason-Merchant at: darby@xilinx.com or Jennifer Waibel at: jenn@xilinx.com
- European Shows: Andrea Fionda at: andrea.fionda@xilinx.com or Andrew Stock at: andrew.stock@xilinx.com
- Japanese Shows: Yumi Homura at: yumi.homura@xilinx.com
- Asia Pacific Shows: Mary Leung at: mary.leung@xilinx.com