

XC18V  
XC17V  
XC17S

FPGA Configurations



Xilinx offers a full range of configuration memories optimized for use with Xilinx FPGAs. Our PROM product lines are designed to meet the same stringent demands as our high-performance FPGAs and CPLDs, taking full advantage of the same advanced processing technologies. In addition, they were developed in close cooperation with Xilinx FPGA designers for optimal performance and reliability.

**XC18V:** Our in-system re-programmable family provides a feature-rich, fast configuration solution unmatched by any other configuration PROM available today, and provides a cost-effective method for re-programming and storing large Xilinx FPGA bitstreams. This family is JTAG ready and Boundary-Scan enabled for exceptional ease-of-use, system integration, and flexibility.

**XC17V/XC17S:** Our low-cost XC17V and XC17S families are an ideal configuration solution for cost-sensitive applications. XC17V00 PROMs are pin-compatible with our 18V family to allow for a cost-reduction migration path as your production volumes increase. The XC17S family is specially designed to provide a low-cost, integrated solution for our Spartan families of FPGAs.

3.3V Configuration PROMs							
PROM	Density	PD8	SO20	PC20	PC44	VQ44	JTAG ISP
XC1765EL(X)	64Kb	X	X	X	X		
XC17128EL(X)	128Kb	X		X	X		
XC17256EL(X)	256Kb	X		X	X		
XC17512L	512Kb	X	X	X			
XC1701L	1Mb	X	X	X			X
XC1702L	2Mb				X	X	
XC1704L	4Mb				X	X	
XC18V128	128Kb				X	X	X
XC18V512	512Kb		X	X		X	X
XC18V01	1Mb		X	X		X	X
XC18V02	2Mb				X	X	X
XC18V04	4Mb				X	X	X

Note: XC1700EL parts are marked with an "X" instead of "EL"

3.3V Configuration PROMs for Spartan-XL/Spartan-II						
FPGA	Configuration Bits	Family	PROM Solution	PD8	V08	SO20
XCS05XL	54,544	Spartan XL	XC17S05XL	X	X	
XCS10XL	95,752	Spartan XL	XC17S10XL	X	X	
XC2S15	197,696	Spartan II	XC17S15XL	X	X	
XCS20XL	179,160	Spartan XL	XC17S20XL	X	X	
XCS30XL	249,168	Spartan XL	XC17S30XL	X	X	
XC2S30	336,768	Spartan II	XC17S30XL	X	X	
XCS40XL	330,696	Spartan XL	XC17S40XL	X	X*	X
XC2S50	559,232	Spartan II	XC17S50XL	X		X
XC2S100	781,248	Spartan II	XC17S100XL	X		X
XC2S150	1,041,128	Spartan II	XC17S150XL	X		X
XC2S200	1,335,872	Spartan II	XC17S200XL*	X		X

\* In development

Configuration PROMs for Virtex-E							
FPGA	Configuration Bits	XC17xx/XC18Vxx Solution	PD8	PC20	SO20	PC44	VQ44
XCV50E	630,048	01	X**	X	X		X***
XCV100E	863,840	01	X**	X	X		X***
XCV200E	1,442,106	02				X	X
XCV300E	1,875,648	02				X	X
XCV400E	2,693,440	04				X	X
XCV405E	3,430,400	04				X	X
XCV600E	3,961,632	04				X	X
XCV812E	6,519,648	04+04 or 08*				X	X
XCV1000E	6,587,520	04+04 or 08*				X	X
XCV1600E	8,308,992	04+04 or 08*				X	X
XCV2000E	10,159,648	04+08* or 16*				X	X
XCV2600E	12,922,336	16*				X	X
XCV3200E	16,283,712	16*				X	X

\* In development

\*\* Available on XC17xx only

\*\*\* Available in XC18Vxx only

Configuration PROMs for Virtex							
FPGA	Configuration Bits	XC17xx/XC18Vxx Solution	PD8	PC20	SO20	PC44	VQ44
XCV50	558,048	01	X*	X	X		X**
XCV100	780,064	01	X*	X	X		X**
XCV150	1,038,944	01	X*	X	X		X**
XCV200	1,334,688	02				X	X
XCV300	1,750,656	02				X	X
XCV400	2,544,896	04				X	X
XCV600	3,606,816	04				X	X
XCV800	4,714,400	04 + 512				X	X
XCV1000	6,126,528	04 + 02				X	X

\* Available on XC17xx only

\*\* Available on XC18Vxx only