DataSource CD-ROM Q1-02 Contents

Xcell Journal Online

- Products Guide
- Product Data Sheets
- Package Drawings
- Packaging and Thermal Characteristics
- Application Notes
- White Papers
- Software/Hardware Manuals
- **Xcell Journal Online**
- Xcell Journal Archives
- Inside Out Columns
- Glossary of Terms
- Literature Request Form
- **Start Here Page**

By Date

Date	Article Description	Issue No.
03/01/02	View from the top **Interesting Times at Xilinx** NEW! *by Wim Roelandts, CEO, Xilinx, Inc.*	42
03/01/02	**The New Era of Programmable Systems** NEW! *by Babak Hedayati, Sr. Director of Product Solutions and Partnerships, Xilinx, Inc.*	42
03/01/02	**Virtex-II Pro FPGAs: The Platform for Programmable Systems Has Arrived** NEW! *by Anil Telikepalli, Marketing Manager, Worldwide Marketing, Xilinx, Inc.*	42
03/02/02	**IBM's New PowerPC Strategy Roadmap** NEW! *by Kalpesh Gala and Mike Vowell, IBM*	42
03/02/02	**The PowerPC Architecture: A Programmer's View** NEW! *by Anthony Marsala, IBM*	42
03/02/02	**Best-of-Class Embedded Software Development Tools** NEW! *by Jay Gould, Embedded Partners Program Manager, Xilinx, Inc.*	42
03/02/02	**Programmable Solutions for Set-Top Boxes** NEW! *by Amit Dhir, Manager, Strategic Solutions, Xilinx, Inc.*	42
03/02/02	**Digital Consumer Convergence Demands Reprogrammability** NEW! *by Robert Bielby, Sr. Director, Strategic Solutions, Xilinx, Inc.*	42
03/02/02	**Bring on the Music – But Take out the Noise** NEW! *by Warren Miller, VP of Marketing, Avnet Design Services*	42
03/02/02	**Platform FPGAs Take on ASIC SOCs** NEW! *by Milan Saini, Technical Marketing Manager, Xilinx, Inc.*	42
03/01/02	**New Configuration Options for Virtex-II Pro** NEW! *by Frank L. Toth, Marketing Manager, Configuration Solutions Division, Xilinx, Inc.*	42
03/02/02	**Use Rocket I/O Multi-Gigabit Transceivers to Double Your FPGA Bandwidth** NEW! *by Brian Von Herzen, Ph.D. -- CEO, Rapid Prototypes, Inc.*	42
03/01/02	**Multiprotocol Modular Engineering Solutions Platform Offers Design Flexibility and Faster Time to Market** NEW! *by Bill Pratt, Regional Applications Manager, Nu Horizons Electronics*	42
03/01/02	**Programmable Logic Enables Digital Displays** NEW! *by Mike Nelson, Senior Manager, Strategic Solutions, Xilinx, Inc.*	42
03/01/02	**Of The 100 Best Companies to Work for... Xilinx is Number 6.** NEW! *by Xilinx Staff*	42
03/01/02	**Low-cost Digital Video IEEE 1394 Hardware Reference Design** NEW! *by Xilinx Staff*	42
03/01/02	**Virtex-II FPGAs –- Product Of The Year** NEW! *by Xilinx Staff*	42
03/01/02	**New CoolRunner-II CPLD Development Kit** NEW!	42

	by Xilinx Staff	
03/01/02	**FlexBench Tool Suite Relies on Xilinx Silicon and Software** NEW! *by Marco Pavesi, Head of Innovative Designs Laboratory, Italtel SpA*	41
03/01/02	**SignOnce and Break the IP License Barrier** NEW! *by Mark Bowlby, AllianceCORE Program Manager, Xilinx, Inc.*	42
03/01/02	**ISE 4.2i Expands High-Powered Design Productivity** NEW! *by Lee Hansen, Software Product Marketing Manager, Xilinx Inc.*	42
03/01/02	**Your Reconfiguration Is in the E-Mail** NEW! *by Marc Defossez, Senior Staff Applications Enginee*	42
03/01/02	**scc-II Microsequencer – A New Solution for Platform FPGA Designs** NEW! *by Aki Niimura, Consultant, Ponderosa Design*	42
03/01/02	**Parallel Cable IV Connects Faster and Better** NEW! *by Theresa Vu, Product Marketing Engineer, Xilinx, Inc.*	42
03/02/02	**Get up to Multi-Gigabit Speed with the SPECCTRAQuest Design Kit** NEW! *by Donald Telian, Technologist, Cadence Design Systems*	42
03/02/02	**Upgrade to Synopsys FPGA Compiler II Synthesis Tool to Maximize Virtex-II Pro Performance** NEW! *by Jackie Patterson, Director of Marketing Programs, Synopsys, Inc.*	42

- **Q4-2001 Archive (Issue 41)**
- **Q3-2001 Archive (Issue 40)**