

	Feature	ISE WebPACK	ISE BaseX	ISE Foundation	ISE Alliance
<b>Devices</b>	Virtex™ Series	<b>Virtex-E:</b> V50E – V300E <b>Virtex-II:</b> 2V40 – 2V250 <b>Virtex-II Pro:</b> 2VP2	<b>Virtex:</b> V50 – V300 <b>Virtex-E:</b> V50E – V300E <b>Virtex-II:</b> 2V40 – 2V250 <b>Virtex-II Pro:</b> 2VP2	ALL	ALL
	Spartan™ II/III Families	ALL (except XC2S400E and XC2S600E)	ALL	ALL	ALL
	CoolRunner™ XPLA3 / CoolRunner-II	ALL	ALL	ALL	ALL
	XC9500™ Series	ALL	ALL	ALL	ALL
<b>Services</b>	Educational Services	Yes	Yes	Yes	Yes
	Design Services	Sold as an Option	Sold as an Option	Sold as an Option	Sold as an Option
<b>Design Entry</b>	Support Services	Web Only	Yes	Yes	Yes
	Schematic Editor	Yes	Yes	PC Only	No
	HDL Editor	Yes	Yes	Yes	Yes
	State Diagram Editor	Yes	Yes	PC Only	No
	CORE Generator System	No	Yes	Yes	Yes
	PACE (Pinout and Area Constraint Editor)	Yes	Yes	Yes	Yes
	Architecture Wizards	No	Yes	Yes	Yes
	DCM – Digital Clock Management MGT – Multi-Gigabit Transceivers				
	3rd Party RTL Checker Support	Yes	Yes	Yes	Yes
	Xilinx System Generator for DSP	No	Sold as an Option	Sold as an Option	Sold as an Option
<b>Embedded System Design</b>	GNU Embedded Tools	Yes	Yes	Yes	Yes
	GCC – GNU Compiler GDB – GNU Software Debugger				
	WindRiver Xilinx Edition Development Tools Diab C/C++ Compiler SingleStep Debugger visionPROBE II target connection	No	Sold as an Option	Sold as an Option	Sold as an Option
<b>Synthesis</b>	Xilinx Synthesis Technology (XST)	Yes	Yes	Yes	No
	Synplicity Synplify/Pro	Integrated Interface	Integrated Interface	Integrated Interface (PC Only)	Integrated Interface (PC Only)
	Synplicity Amplify Physical Synthesis Support	Yes	Yes	Yes	Yes
	Leonardo Spectrum	Integrated Interface	Integrated Interface	Integrated Interface	Integrated Interface
	Synopsys FPGA Compiler II	EDIF Interface	EDIF Interface	EDIF Interface	EDIF Interface
<b>Implementation</b>	ABEL	CPLD	CPLD	CPLD (PC Only)	No
	iMPACT	Yes	Yes	Yes	Yes
	FloorPlanner	Yes	Yes	Yes	Yes
	Xilinx Constraints Editor	Yes	Yes	Yes	Yes
	Timing Driven Place & Route	Yes	Yes	Yes	Yes
	System ACE Configuration Manager	Yes	Yes	Yes	Yes
	Modular Design	No	Sold as an Option	Sold as an Option	Sold as an Option
	Timing Improvement Wizard	Yes	Yes	Yes	Yes
	IBIS Models	Yes	Yes	Yes	Yes
	STAMP Models	Yes	Yes	Yes	Yes
<b>Board Level Integration</b>	LMG SmartModels	Yes (Available from Synopsys)	Yes (Available from Synopsys)	Yes (Available from Synopsys)	Yes (Available from Synopsys)
	HSPICE Models*	Yes	Yes	Yes	Yes
	HDL Bencher™	Yes	Yes	PC Only	No
<b>Verification</b>	ModelSim™ Xilinx Edition (MXE II)	ModelSim XE II Starter**	ModelSim XE II Starter**	ModelSim XE II Starter**	ModelSim XE II Starter**
	Static Timing Analyzer	Yes	Yes	Yes	Yes
	ChipScope PRO	No	Sold as an Option	Sold as an Option	Sold as an Option
	FPGA Editor with Probe	No	Yes	Yes	Yes
	ChipViewer	Yes	Yes	Yes	Yes
	XPower (Power Analysis)	Yes	Yes	Yes	Yes
	3rd Party Equivalency Checking Support	Yes	Yes	Yes	Yes
	SMARTModels for PPC and Rocket I/O	No	Yes	Yes	Yes
	3rd Party Simulator Support	Yes	Yes	Yes	Yes
	<b>Platforms IP/CORE</b>		PC (MS Windows 2000/MS Windows XP) PC Only (MS Windows 2000/MS Windows XP)	PC (MS Windows 2000/MS Windows XP) PC Only (MS Windows 2000/MS Windows XP)	PC (MS Windows 2000/MS Windows XP) Sun Solaris, Linux

\* HSPICE Models available at the Xilinx Design Tools Center at [www.xilinx.com/ise](http://www.xilinx.com/ise). \*\* MXE II supports the simulation of designs up to 1 million system gates and is sold as an option. For more information, visit the Xilinx Design Tools Center at [www.xilinx.com/ise](http://www.xilinx.com/ise)