



### Xilinx Design Services:

Accelerate Your Productivity





#### Xilinx Design Services Mission

The Xilinx Design Services

mission is to increase your

competitive advantage by

offering the optimal system-

level benefits gained from

programmable logic solutions

using systems architecture

consulting, hardware engineering,

and embedded software design.





DESIGN SERVICES Xilinx Global Services

#### In The Race To Market, Experience Counts

Time is money, and in the race to market, the company that gets there first earns a greater share of the market. Reducing your design time drives tangible bottom–line benefits, such as increased profitability and a greater return on your R&D investments – plus you get your product to market before your competition.

Xilinx<sup>®</sup> Design Services (XDS) helps accelerate your time to market by augmenting your design team with industry experts in FPGA design techniques and solutions. Led by professionals with proven project management skills, our industry-recognized FPGA hardware engineers, embedded software designers, product specialists, and system architects will make the difference. Together, your design team and our XDS specialists can get your products to market faster, more cost-effectively, and with optimal performance.

#### **The Xilinx Design Services Portfolio**

Whether it's helping you create your product from an initial concept, implementing your specifications, modifying existing intellectual property cores, or improving product performance, Xilinx Design Services provides extensive FPGA hardware and embedded software design experience backed by industry recognized experts and resources to solve even the most complex design challenge.

**System Architecture Consulting** – Provide engineering services to define system architecture and partitioning for design specification.

**Custom Design Solutions** – Project designed, verified, and delivered to mutually agreed upon design specifications.

**IP Core Development, Optimization, Integration, Modification, and Verification –** Modify, integrate, and optimize customer intellectual property or third party cores to work with Xilinx technology. Develop customer-required special features to Xilinx IP cores or third party cores. Perform integration, optimization, and verification of IP cores in Xilinx technology.

**Embedded Software –** Develop complex embedded software with real-time constraints, using hardware/software co-design techniques.

**Conversions** – Convert ASIC designs and other FPGAs to Xilinx technology and devices.



#### **The XDS Difference**

- Professional project management
- System-level experience around the world
- Faster project ramp-up
- Experienced FPGA design engineers
- FPGA hardware and software experts
- Accelerated knowledge of FPGA systems
- Access to ready-made intellectual property cores

#### **The Xilinx Advantage**

Customer satisfaction comes first. We work hard to ensure that our commitments are met and to build long-term partnerships with our customers. With partnership comes flexibility, the type you can rely on to respond quickly to emerging business opportunities. By partnering with Xilinx, you get a complete programmable logic design solutions that help you get your products to market faster, more cost effectively, and with higher quality than your competition. Aggressive innovation is what sets Xilinx apart.

No other company can provide complete FPGA hardware and software design solutions supported by as much experience and expertise. Xilinx Global Services provides you with global technical support, Education Services and a portfolio of Design Services to help you finish faster.

#### **Find Out For Yourself**

To find out more, email us at: designservices@xilinx.com or, visit our website at support.xilinx.com/xds

#### **Comprehensive Project Management**

## Design Concept Co

 System Overview Standards Compliance Features List Schedule Requirements

Non-Disclosure
 Agreement
 Technical Requirements
 System Review
 Design Re-use

#### Contract Agreement

- Professional Services Agreement Frame Agreement Terms and Conditions
- Requirements Specification System Requirements Features Support Interface Agreement Device Selection
- Statement of Work
  Scope of Work
  Deliverables
  Schedule & Milestones
  Responsibilities
  Quotation

#### Design Implementation & Verification

- Project Planning
  Design Specification
  Micro-Architecture Definition
  Test Document
- Design Implementation Hierarchical Definition Design Capture
- Testbench Development System Modeling Test Suite Design
- Simulation and Sign-off
  Design Synthesis
  Constraints Definition

#### Delivery

- Delivery Package Release Document Test Document Design Database
- Customer Acceptance
  Deliverable Review
  Warranty Period

#### Xilinx Design Services: Expertise and Experience

# Xilinx Global Services



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#### **Telecommunication & Networking**

#### ATM

- Adaption layer processing
- Switching
- Termination, routing
- UTOPIA
- · Ethernet/gigabit ethernet
- PPP termination
- MAC layer processing
- SONET/SDH
  - Framers/deframers
  - TOH extraction/insertion
  - Data path controllers
  - IP over ATM/SONET
- Frame relay/ISDN
- 2 & 3 GPP
- TDM
- HDLC controllers
- FE0
- Reed-Solomon
- Viterbi
- Modulators

#### **Xilinx Advanced Technologies**

- XtremeDSP<sup>\*\*</sup>
- Internet Reconfigurable Logic (IRL)™
- · Emerging connectivity standards
- Microprocessor solutions
- Newest FPGA architectures

#### **FPGA-Based DSP & Math Functions**

- Data compression
- FIR/IIR filters
- Nyquist filters
- · Decimating filters
- · Polyphase filters
- Interpolators
- Modulators/demodulators
- Correlators
- Matrix multipliers

#### **High Speed Interfaces**

- CSIX
- FLEXbus4<sup>™</sup>
- UTOPIA master/slave
- POS-PHY
- IX Bus
- SPI-4
- GMII/XGMII
- PCI
- · PCI-X

#### Video/Image Processing

- Scan rate conversion
- Frame buffer controllers
- Video filters
- · CCD interfaces
- · Pattern matching
- DVB
- STB

FORTUNE 2001 100 BEST COMPANIES TO WORK FOR