

Packages and Thermal Characteristics

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Package Drawings Table of Contents (Links to individual package drawings)

Application Notes, White Papers, and Other Technical Information

Application Notes Summaries (Links to complete Application Notes)

White Paper Summaries (Links to complete White Papers)

IBIS Files (Accessed from Data Book CD-ROM—directory: IBIS_files)

BSDL Files (Accessed from Data Book CD-ROM—directory: BSDL_files)

Verilog Files (Accessed from Data Book CD-ROM—directory: Xapp_Veril_VHDL_files)

VHDL Files (Accessed from Data Book CD-ROM—directory: Xapp_Veril_VHDL_files)

Software Manuals (Accessed from Data Book CD-ROM—directory: sw_manuals)

Xcell Journal:

The Authoritative Journal for Programmable Logic

Xilinx Product Literature**Newsgroup: comp.arch.fpga**

An archive of over 30,000 postings.

<http://www.fpga-faq.com/archives/index.html>

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