

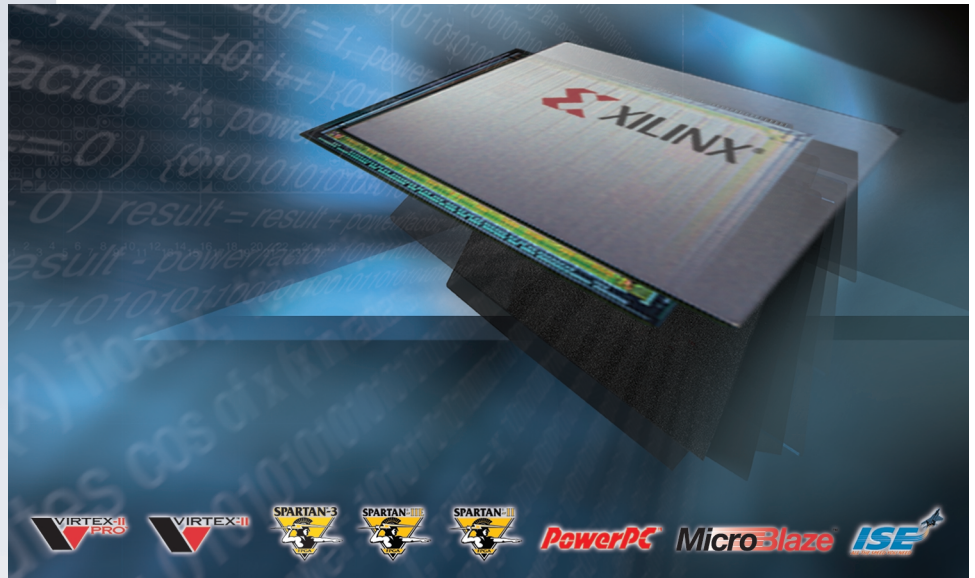


Embedded Development Kit

Programmable Systems Design is now Easier than Ever

Developing high performance programmable systems with embedded processors and high-speed serial transceivers can be quite challenging and time consuming – if you start from scratch. You have to develop and integrate both the hardware and software, with or without a real time operating system, create I/O interfaces, integrate in-house or third-party cores, and debug issues before you can even test your design.

Now, with the Embedded Development Kit, you can dramatically accelerate the hardware and software platform design and debugging of your next product, while reducing cost and overall development time. Within the powerful Xilinx® ISE Platform Studio (XPS) Environment, you are free from the mundane details of hardware and software design and can focus on the overall architecture to create the best design for your particular application. Now you have a single environment for defining a custom hardware platform, its matching software platform optimized, firmware embedded C libraries with a complete board support package (BSP).



Save Time and Money

When you have the right tools, you can complete your designs much faster, and that means you'll save development costs, create better designs, and improve profitability. The Embedded Development Kit (EDK) has all the components needed to design the hardware and software platforms of an embedded system. It is the only way for you to develop Programmable Systems design because now you can:

- **Create Designs Faster** – EDK gives you have a complete programmable systems development environment – the ISE Platform Studio – designed for both hardware and software developers. The ease with which you match a software platform to any custom hardware platform ensures you can quickly provide not only the underlying hardware, but the matching software as well.
- **Create Lower Cost Designs** – When you save time, you also save development costs by allocating manpower where it gives you the most return on your investment. You can increase productivity and improve profitability by shifting design resources between hardware and software within the same design environment.
- **Create Better Designs** – Because you have more time you can create different designs, platforms and interfaces for different IP. You can optimize your design, add or delete features and evaluate architectural tradeoffs.
- **Create More-profitable Designs** – When you get to market faster, your product has a longer life and that means greater profitability. There is no faster way to complete your designs, create better designs, and get your designs to market.

Xilinx Platform Studio (XPS)

Graphical and command line tools for developing and debugging the hardware and software platforms for an embedded application. Hardware platform includes graphical and textual definition tools and generation of simulation and implementation netlists for use with the ISE logic design tools. Software platform definition includes graphical and textual tools for matching it to the hardware platform, editing source code, running the compiler tool chains and library generation.

Software Development Tools

- GNU C/C++ compiler for MicroBlaze™ and PowerPC™ (gcc)
- GNU Debugger for MicroBlaze and PowerPC (gdb)
- Other GNU utilities
- Integrated with Wind River XE (Xilinx Edition) for PowerPC designs
- XMD – Xilinx Microprocessor Debug engine for MicroBlaze and PowerPC. It provides host-based target control using command line tools that enable complex regression testing.
- Data2BRAM – a stand alone application for loading and updating on-chip memory content directly within the FPGA bitstream. It is also integrated with the Wind River XE (Xilinx Edition) for PowerPC designs.

Board Support Packages (BSPs)

- Stand Alone BSP - For non-RTOS systems (MicroBlaze and PowerPC)
- Wind River VxWorks – For Virtex-II Pro™ PowerPC systems

Processor IP (over sixty Parameterizable cores)

- PowerPC and MicroBlaze infrastructure and peripheral IP cores (CoreConnect™ Processor Local Bus (PLB) and On-Chip Peripheral Bus (OPB) infrastructure cores)
- Evaluation versions of high value CoreConnect cores (EMAC 10/100, IIC Master Slave, HDLC Single Channel Controller, UART 16450, 16550 etc)

MicroBlaze soft processor core

- Industry's fastest 32-bit soft processor core

Embedded Development Kit Contents:

The EDK works in both PC and workstation environments, and includes:

- Embedded Development Kit CD
- ISE 5.1i Design environment evaluation CDs
- Alliance Partner CDs (Trial versions of software tools, documentation, data sheets, etc., from third-party vendors supporting the Xilinx embedded processor solutions)

What Can you Do With the Embedded Development Kit?

- Define custom hardware platform for programmable system using processor cores (PowerPC or MicroBlaze), Parameterizable IP and interconnect bus
- Use industry standard software development and debug tools (GNU, Wind River Systems (XE from Xilinx or full-editions from Wind River)
- Develop software platform to match custom hardware
- Verification support and interface to Xilinx supported HDL simulators

Embedded Development Kit Pricing

Part Number	Description	Pricing
DO-EDK	Embedded Development Kit	Contact your local Xilinx representative for pricing and availability

For the latest information on the Embedded Development Kit go to: www.xilinx.com/edk

For more information on Xilinx Processor Solutions visit: www.xilinx.com/processor



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FORTUNE 2003
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