New WebPACK–
Packs a CPLD Punch

WebPACK is a collection of downloadable modules that give you the best possible CPLD development solution while offering complete EDA tool flexibility

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Xilinx has taken the industry's best CPLD implementation tool suite, coupled it with the latest enhancements in ABEL and HDL synthesis technology, and incorporated JTAG programming capability, in a free, downloadable solution called WebPACK™.

WebPACK Modules

WebPACK includes three, self contained, downloadable modules. Each module can be downloaded, installed, and run individually, and works in conjunction with other EDA tools, such as synthesis, simulation and schematic capture programs. This gives you the flexibility to use your existing tools and simply interface them to our CPLD solution.

When two or more of the modules are downloaded and installed, they become an integrated design environment. This allows you to go from entry, synthesis, and optimization to device fitting and programming, all from within a single environment.

WebPACK Project Navigator, Simulation Waveform Viewer, and Color-coded HDL Editor
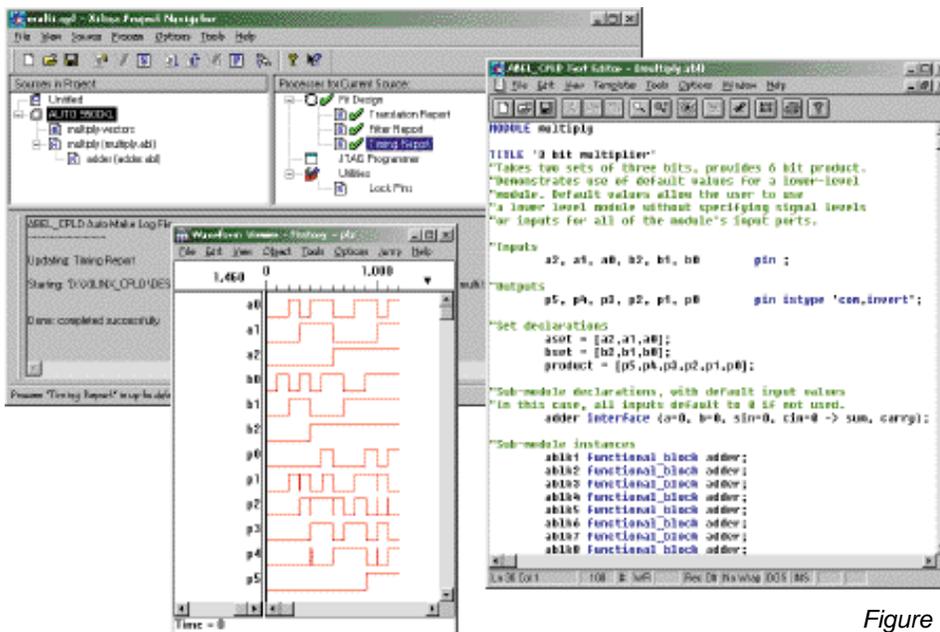

Figure 1

XC9500 HDL-ABEL Synthesis Module

This module (Figure 1) provides the latest version of ABEL v7.1, Xilinx Synthesis Technology, and a project navigation system specifically designed for the XC9500 series. These two synthesis engines provide VHDL, Verilog, and ABEL language design entry within our color-coded HDL editor.
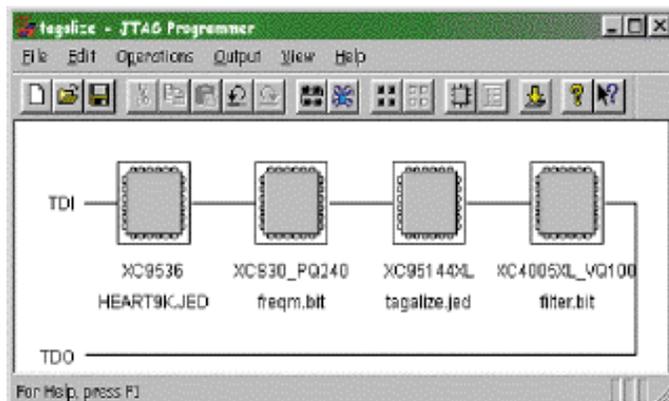
XC9500 Fitting Tools Module

This module (Figure 2) includes the traditional CPLD implementation tools. Included are the Xilinx Design Manager and Flow Engine, Constraints Editor, Iterative Timing Analyzer, and the newly introduced CPLD ChipVIEWER™. These tools can be used in conjunction with the HDL-ABEL Synthesis module or in a more traditional environment as part of an overall EDA solution with other 3rd-party tools.

Device Programming Module

This module (Figure 3) consists of our JTAG Programmer software for both CPLDs and FPGAs. This tool can be run stand-alone to support any Xilinx CPLD or FPGA application and is automatically run from within the WebPACK project navigator, if installed.

Conclusion

Each of these free downloadable modules, as well as the WebFITTER on-line CPLD fitting and evaluation tool, are available from the Xilinx website at: www.xilinx.com/products/software/webpowered.htm. Both tools offer the easiest and most cost effective way to evaluate, price, design, and implement a Xilinx XC9500 CPLD solution. **Σ**

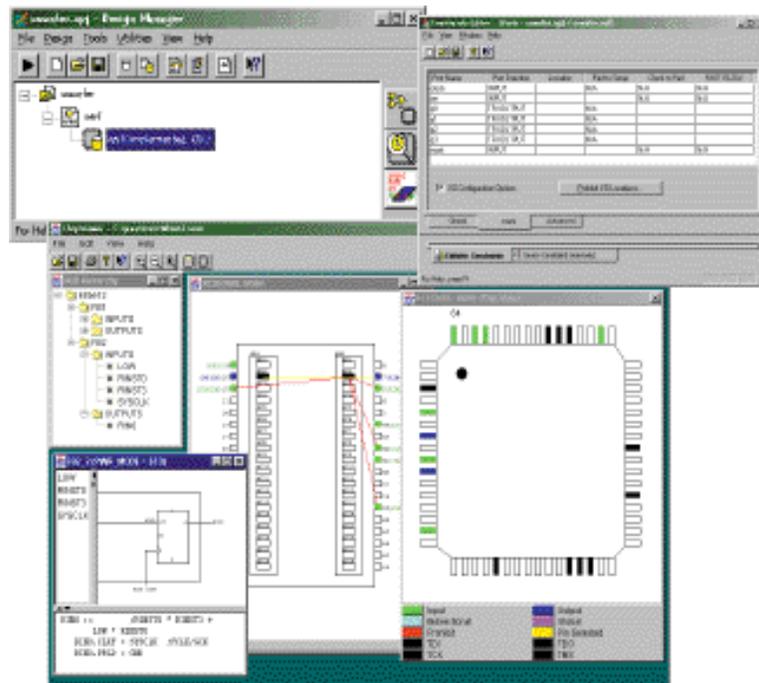Xilinx Design Manager, Constraints Editor and CPLD ChipViewer.

Figure 2

Xilinx JTAG Programmer Graphical User Interface

Figure 3

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