



## Corporate Fact Sheet

### COMPANY INFORMATION

Founded in 1984, Xilinx is the world's largest supplier of programmable logic solutions producing industry-leading device architectures and world class design software. Headquartered in San Jose, CA, the company pioneered the market for field programmable gate array (FPGA) semiconductor devices that provide high integration and quick time-to-market for electronic equipment manufacturers in the computer, peripheral, telecommunications, networking, industrial control, instrumentation, and high-reliability markets.

### FINANCIAL INFORMATION

Publicly traded on NASDAQ • Symbol: XLNX  
 Shares outstanding: 79 million, as of 12/31/95  
 3 for 1 stock split in August 11, 1995  
 Fiscal Year 1995 revenues: \$560.8M  
 Fiscal Year 1995 net income: \$101.4M

### INDUSTRY RANKING

1996, #1 PLD worldwide supplier, PACE Technologies  
 1997, #8 ASIC worldwide supplier, Dataquest  
 1996, *Fortune* 100 Fastest Growing American Companies  
 1995, Kachina Award: Best Financially-Managed Company (5th consecutive year), InStat  
 Included in *Forbes* 500 and *Business Week* Global 1000

### CORPORATE HEADQUARTERS AND FACILITIES

*San Jose, CA:* Xilinx's research and development, sales, marketing, administration, customer service, and financial operations.

*Boulder, CO:* Facility for the software research and development operations

*Dublin, Ireland:* Additional manufacturing and engineering for European customers

*England, Germany, Asia, Japan:* International sales offices

Outside North America, Xilinx sells its products through direct-sales offices and representatives and distributors in 36 countries.

### PERSONNEL

More than 1300 full-time employees total (as of 4/1/96): approximately 75% in San Jose; 11% in Ireland; and less than 10% each in Colorado, Europe, Asia, and Japan

### PRODUCT HIGHLIGHTS

#### **Field Programmable Gate Array (FPGA) families**

An FPGA device is a highly integrated semiconductor logic device which can be programmed by the end user in the field. Devices can be custom programmed according to unique requirements and re-programmed as applications requirements change.

**XC3100** up to 6,000 usable gates for high performance, low density applications

**XC4000X** up to 250,000 usable gates for high-performance, high-density applications requiring RAM

**XC6000** up to 100,000 usable gates with coprocessor interface for DSP applications

#### **Complex Programmable Logic Device (CPLD) families**

Complementing the FPGA selection are the CPLD families which offer simpler software and more predictable timing—available in EPROM and Flash technologies.

**XC9000** up to 588 macrocells; 5V flash-based for ISP applications

#### **Design and Implementation Software**

Xilinx has chosen an open systems approach that allows its customers to choose the best design and programming tools available on the market today.

**Xilinx XACTstep Software Solutions** provide all of the implementation technology required to design with Xilinx logic devices, including module generation, design optimization and mapping, placement and routing, timing analysis, and program file generation. The XACTstep Software Solutions consist of three series of products:

**Alliance Series:** Through engineering and marketing relationships with the leading EDA software suppliers such as Aldec, Cadence, Data I/O, Exemplar, Mentor Graphics, Synopsys and Viewlogic, this open systems strategy extends to front-end design creation, synthesis and verification. The result has been the creation of complementary technology and tightly integrated third-party links with Xilinx's back-end implementation software.

**Foundation Series:** A fully integrated package that supports a broad range of CPLD and FPGA design requirements. The easy-to-use Windows-based software provides access to industry standard Hardware Description Languages (HDLs), synthesis, schematic entry, gate-level simulation, and the Xilinx implementation tools.

**System Level Integration (SLI) Series:** This product series enables the application of system designs in high density, high performance programmable logic. The first offering is the **LogiCore** family of fully verified drop-in PCI modules that allow designers to cut design time and significantly reduce design risk while having access to the best performance and lowest component cost available. Follow-on LogiCore products will include DSP filters and multipliers.

#### **Conversion programs**

**HardWire** mask-programmed products for lowering the cost of a design in high volume production

#### **STRATEGIC PARTNERSHIPS AND PROGRAMS**

Xilinx maintains maximum flexibility through the use of outside wafer fabrication facilities (fabs). Since 1985, Xilinx has had a manufacturing relationship with Seiko Epson with initial devices on a 2.0  $\mu\text{m}$  process using 4-inch wafers. Products are now being developed beyond a 0.5  $\mu\text{m}$  process using 8-inch wafers. Xilinx continues to invest in Seiko to secure wafer capacity. Other fab partners include Yamaha and IC Works.

Xilinx reached a definitive agreement with United Microelectronics Corporation (UMC) and other joint ventures to form a separate Taiwanese company to build a semiconductor manufacturing facility in Taiwan to secure capacity. The new fab will produce 8-inch wafers utilizing UMC's submicron CMOS process technology. The fab will begin production by 1997.

A five-year partnership with Synopsys, the market leader in synthesis, was announced in 1994 aimed at improving high-level design methodology and silicon efficiency.

**Developer's Program:** Xilinx announced this program to offer special support for qualified companies using Xilinx programmable logic. Members can qualify for discounts on devices, development systems, and training as well as support of promotional activities. For a program application, developers may call the Xilinx Literature Hotline at (800) 231-3386 or fax requests to (408) 879-4780.

#### **NOTABLE ACQUISITIONS**

In April 1995, Xilinx acquired NeoCAD, Inc., a developer of high performance design software for programmable logic, to provide Xilinx's large customer base with immediate access to even more powerful CPLD and FPGA software design solutions.

#### **MANAGEMENT**

*Bernie Vonderschmitt*, Chairman of the Board and Cofounder

*Willem Roelandts*, President and Chief Executive Officer

*Gordon Steel*, Vice President and Chief Financial Officer

*Bill Carter*, Chief Technical Officer

*Richard Sevcik*, Sr. Vice President, Software

*Scott Brown*, Senior Vice President, Worldwide Sales

*Chuck Fox*, Vice President and General Manager, HardWire Business Unit

*Evert Wolsheimer*, Vice President and General Manager of CPLD Division

*Dennis Segers*, Vice President and General Manager, High-End FPGAs and Vice President FPGA Product Development

*Sandeep Vij*, Vice President and General Manager, High-Volume FPGAs and Vice President Marketing

#### **PRESS CONTACTS**

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