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FOR IMMEDIATE RELEASE

XILINX UNVEILS XILINX ONLINE PROGRAM

Adobe Photoshop demonstration uses Internet and Virtex FPGAs to upgrade PC

SAN JOSE, Calif., May 25, 1999— In conjunction with a new reconfigurable demonstration and customer endorsements Xilinx, Inc. (NASDAQ:XLNX) announced today the Xilinx Online program to enable, identify, and promote field upgradable applications based upon the award winning Internet Reconfigurable Logic (IRL) methodology from Xilinx. These field upgradable applications are defined as being connected to a private or public network with the ability to be updated, fixed, or modified *after* they have been deployed in the field. Many Xilinx Online field upgradable applications use Xilinx Virtex devices for their size, speed, and flexibility. Such applications are being widely developed today and this new demonstration will be available at the Design Automation Conference in June in Xilinx booth #2532.

"In the demonstration application which provides hardware acceleration for Photoshop filters, we wanted to show a complete system using standard tools available today for doing field upgrades," said Rich Sevcik, senior vice president software, cores and support at Xilinx. "With our tools, the Virtex series, our design service partners, and the reference designs, we are providing a complete environment to enable our customers to build the next wave of programmable logic applications."

"Our customers include many of the world's space agencies and leading aerospace firms," said Toby Bennett, Vice President, TSI TelSys. "Their satellite ground station facilities, which utilize our protocol agile communication products, are often located in remote, inhospitable regions such as near the North Pole. TSI TelSys develops Xilinx Online applications so that our systems can be easily maintained and updated over the Internet."

"Field upgradability is a factor in choosing a programmable device," said Tod Rikley, Hardware Development Manager at NorTel Networks, Belleville, Ontario. "In a number of our customer trials, our use of the download capabilities of the FPGA made it possible to offer our customers a hardware fix through software thereby avoiding a costly retrofit."

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XILINX RELEASES TOOLS FOR CREATING XILINX ONLINE APPLICATIONS

JBits, ChipScope, and Java API for Boundary Scan tools now available

for upgrading Virtex FPGAs deployed in the field

SAN JOSE, Calif., May 25, 1999—Facilitating the creation of Xilinx Online field upgradable applications, Xilinx, Inc. (NASDAQ:XLNX) announced the availability of JBits, ChipScope and the Java application programming interface (API) for Boundary Scan tools. These tools, previously announced as part of the award-winning Internet Reconfigurable Logic (IRL) methodology from Xilinx, enable the network delivery of design updates to programmable logic devices already deployed in the field. Xilinx also announced the addition of Memec Design Systems and Cadence Design Systems, along with previously announced Siemens, to its array of design support providers. Xilinx Online applications will be demonstrated at the Design Automation Conference in June in Xilinx booth #2532.

"As committed in our October announcement, we are now delivering the tools that make field upgradability via the Internet a reality," said Rich Sevcik, senior vice president software, cores and support. "Our customers can begin taking advantage of the tremendous time-to-market benefits and the flexibility of field upgradability today by using our tools and the design services available from many of our business partners."

"The advent of commercially available online reprogrammability for FPGAs expands the range of reconfigurable applications," said Jim Douglas, vice president and general manager of Cadence Embedded Systems Design Group. "Cadence's expertise in embedded systems design enables customers to rapidly capitalize on this expanding market. In fact, our Java expertise was used in the development of critical functionality for the Xilinx Online tools."

"As usual, Xilinx is leading the way with tools, examples, and technology into this exciting and lucrative new marketplace—field upgradable applications through a network," said Timothy Smith, managing director at Memec Design Systems. "We have seen a huge interest among our customers for these Xilinx Online enabling tools."

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FOR IMMEDIATE RELEASE

**XILINX WINS ENGINEERING CONSORTIUM AWARD
FOR DEVELOPMENT OF INTERNET RECONFIGURABLE LOGIC**

SAN JOSE, Calif., May 25, 1999—Citing market impact and customer benefit, the International Engineering Consortium has selected the Internet Reconfigurable Logic system from Xilinx, Inc. (NASDAQ:XLNX) as a winner of the organization's 1999 InfoVision Award. Xilinx won for the Internet category and will be recognized with other winners at an award ceremony in October in Chicago during the National Communications Forum, a networking and communications conference.

The InfoVision award marks the second time Xilinx has won worldwide recognition for Internet Reconfigurable Logic (IRL) since it was unveiled last November. *Electronique International Hebdo*, the Paris-based weekly newspaper for the electronics business in France, chose the IRL methodology as the top technical achievement in the semiconductor industry for 1998.

The IRL methodology combines computer networks, the Java programming language and the new Xilinx Virtex field programmable gate arrays (FPGAs) to create a new class of electronic equipment that can be fixed, modified or updated after installation at the end user's premises. These Xilinx Online field upgradable applications can range from multi-use set-top boxes and wireless telephone cellular base stations to satellite communication systems.

In related news, Xilinx today announced the availability of tools to help customers design Xilinx Online applications that are based on the IRL methodology. The company also announced an expanded network of expert consulting firms that will offer services to assist in the development of Xilinx Online applications, as well as customers actively involved in Xilinx Online applications.

"This exciting technology has clearly resonated with the design engineering community," said Xilinx president and CEO Wim Roelandts. "Since we announced our unique approach for expanding the reach of programmable logic, there has been a growing interest in this exciting technology. IRL is the backbone for creating Xilinx Online field upgradable applications, and we expect these new products to provide tremendous new levels of flexibility for our customers' customers."

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XILINX INTERNET RECONFIGURABLE LOGIC

WINS SECOND AWARD FROM ELECTRONICS PUBLISHING COMMUNITY

SAN JOSE, Calif., May 25, 1999 – Electronique, the monthly magazine of record for the electronics industry in France, has ranked the Internet Reconfigurable Logic (IRL) methodology from Xilinx (NASDAQ:XLNX) as one of the industry's best innovations for 1998. The Xilinx IRL rollout won top honors for active components, one of several product categories recognized for excellence. An independent panel of customers, consultants and design services judged more than 80 products nominated for the magazine's annual awards.

Last November, Electronique International Hebdo, the Paris-based weekly newspaper for the electronics business in France, chose the IRL methodology as the top technical achievement in the semiconductor industry for 1998.

The methodology combines computer networks, the Java programming language and the new Xilinx Virtex field programmable gate arrays (FPGAs) to create a new class of electronic equipment that can be fixed, modified or updated after installation at the end user's premises. These field-upgradable Xilinx Online applications can range from multi-use set-top boxes and wireless telephone cellular base stations to satellite communication systems.

This week Xilinx announced the availability of new tools to help customers design Xilinx Online applications. The company also announced an expanded network of expert consulting firms that will offer services to assist in the development of Xilinx Online applications.

Third-party partners using Xilinx devices for reconfigurable computing

ANNAPOLIS MICRO SYSTEMS, INC.

"Annapolis Micro Systems is very excited to be working with Xilinx on this new program. JBits will allow developers to place applications and updates on the web for downloading into the Annapolis OEM hardware. This will be particularly popular for distributing applications for our WILDCARD™, the first commercially available CardBus™ board to support using a Virtex device for processing on a laptop."
Jane Donaldson, President, Annapolis Micro Systems

Annapolis Micro Systems, Inc. is the leader in Xilinx based High Speed Digital Signal Processing boards, including the new Virtex based WILDSTAR™ family, and the classic Xilinx XC4000 based WILDFIRE™ family. For information contact Jane Donaldson, at jdonald@annapmicro.com, (410) 841-2514, or visit our web at <http://www.annapmicro.com>.

MIROTECH MICROSYSTEMS

"It was only a question of time before someone realized the incredible potential to reconfigure hardware via the Internet. With the IRL strategy, Xilinx is taking a jump-start on their competitors and is setting the pace to which other vendors will have to catch. IRL fits nicely with our product development goals as well, and we look forward to the days when will be able to offer our customers hardware and system upgrades through IRL-based tools." *Pierre Popovic, President, MiroTech Microsystems Inc.*

MiroTech Microsystems, a leader in the SBRC-Single Board Reconfigurable computing arena, has developed an advanced system architecture that accelerates DSP and imaging applications by several folds. This speed up is realized by transparently partitioning applications in hardware and software executable code. Based in Saint Laurent Canada, the company researches, develops and markets High Reconfigurable Computers to system manufacturers and OEMs worldwide. MiroTech is a private company located at 395, boulevard St. Croix, St. Laurent, QC., Canada. <http://www.mirotech.com>.

VIRTUAL COMPUTER CORPORATION

Virtual Computer Corporation, the worldwide leader in Reconfigurable Computer products, provides hardware and software solutions for core verification, hardware/software co-design, and high performance computing system using Xilinx FPGAs. VCC offers a variety of products for workstations and personal computers utilizing Hardware Object Technology™, our patented run-time reconfiguration process. Founded in 1989, VCC won the first SBIR Technology of the Year Award in 1994 for the introduction of Reconfigurable Computing. Virtual Computer Corp., a privately held company is based in Reseda, Calif. For more information contact Virtual Computer Corp. Tel: 818-342-8294; fax: 818-342-0240; email info@vcc.com; or visit our web site at <http://www.vcc.com>.

“We're once again honored to receive this recognition for our IRL methodology," said Xilinx president and CEO Wim Roelandts. “Since we announced our unique approach for expanding the reach of programmable logic, there has been a growing interest in this exciting technology. IRL is the backbone for creating field-upgradable Xilinx Online applications, and we expect these new products to provide tremendous new levels of flexibility for our customers' customers.”

Xilinx is the leading innovator of complete programmable logic solutions, including advanced integrated circuits, software design tools, predefined system functions delivered as cores, and unparalleled field engineering support. Founded in 1984 and headquartered in San Jose, Calif., Xilinx invented the field programmable gate array (FPGA) and commands more than half of the world market for these devices today. Xilinx solutions enable customers to reduce significantly the time required to develop products for the computer, peripheral, telecommunications, networking, industrial control, instrumentation, high-reliability/military, and consumer markets. For more information, visit the Xilinx web site at www.xilinx.com.

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The International Engineering Consortium (IEC) is a cooperative, public service organization dedicated to positive change in the information industry and university communities. For more than fifty years, the IEC has provided educational opportunities for industry professionals, university professors, and promising students. Executives from throughout the industry continue to build strong business and technical foundations for the future through knowledge gained from IEC forums and publications.

This marks the third year the IEC has used the InfoVision awards to recognize the industry's most original products based on innovation, uniqueness, market impact, customer benefit, and value to society.

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Cadence offers customers a broad line of design services, including Java-enabled embedded systems while Memec Design Services and Siemens Information Technology for Industrial Plants are consultant companies that help customers design Xilinx Online applications. For additional information, contact Diana Anderson at Cadence at 408-894-3478 or email to diana@cadence.com or call the help line at 1-800-746-6223. For Memec contact Timothy Smith by phone at 602-491-4311 or email: info@memecdesign.com. For Siemens, contact Miguel Hernandez by phone at 49-89-636-47580 or email: miguel.hernandez@mchr2.siemens.de.

The JBits bitstream programming API is available through third parties: Annapolis Microsystems, Mirotech, and Virtual Computer Corporation. The JBits API is a Java-based tool set that allows designers to write information directly to a Xilinx FPGA to carry out whatever customer logic operations were designed for it. The JBits API permits the FPGA bitstream to be modified quickly, allowing for fast reconfiguration of the FPGA. With Virtex FPGAs, the JBits API can partially or fully reconfigure the internal logic of the device.

Also available is the ChipScope tool, a JBits-based tool that verifies programmable logic over a network; it's available directly from Xilinx. This portable, interactive debugging tool allows designers to examine the operation of Xilinx FPGA circuits; it is designed to show data flow and to display the internal states of all FPGAs in the system. The tool simplifies the tedious design verification required for system-on-a-chip designs.

Xilinx Online field upgradable solutions will be targeted at emerging network appliances, such as multi-use set top boxes, mobile network devices, security systems and process controllers, and in network equipment, such as ATM, cellular base stations, and satellite communications systems. The hardware for these Virtex-based products can be upgraded over the Internet to add new features or capabilities after product deployment.

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Speaking on their downloading procedures for making post-installation upgrades, John Simmons, Project Leader, NDS, Ltd., said, " News Digital Systems (NDS) already makes great use of field upgrades: for example, all our FPGA design data is stored in flash memory and the flash is upgradable via the Ethernet. Field upgradability is essential to our success. It allows fast, cheap resolutions to the rare problems that we get in the field, and is a delight to our customers." NDS provides broadcast systems for HDTV.

This demonstration application allows end users to select Photoshop filters configured for a Virtex FPGA from an Internet appliance website and securely download them using Java-based technology. These filters, designed using Xilinx software, are downloaded to a laptop and then programmed into a Virtex FPGA on a PCMCIA card designed by Annapolis Microsystems for high speed processing.

Virtex FPGAs: The Basis for Upgradable Systems

The Virtex family was developed to address system-level designs and field upgradable systems, integrating 200 MHz system interfaces and timing management capabilities within a million-gate FPGA that includes a hierarchy of memory resources.

Virtex FPGAs support partial reconfiguration, thus allowing new circuitry to be downloaded while standard operation continues within the device. A fast 400Mb/second reconfiguration rate ensures that a full reconfiguration can be done in milliseconds and a partial reconfiguration can be done in microseconds.

Reference designs will be available on the Xilinx Online web page within the Xilinx site. These web pages also contain frequently asked questions, a user forum, application notes, white papers, and links to third-party reconfiguration companies and design service companies supporting the development of Xilinx Online field upgradable applications. The Java API for Boundary-Scan is available now at www.xilinx.com/products/software/sx/sxpresso.html#Java API.

Xilinx Online field upgradable applications are currently being used in network equipment, such as ATM switches, cellular base stations, and satellite communications systems. The Xilinx Online program is additionally targeting opportunities in emerging markets such as network appliances, multi-use set top boxes, and mobile network devices.

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