

## Inside Out Columns

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▶ **Articles by Date**

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Date	Article Description
8/20/01	<b>PowerPC Architecture with Xilinx FPGAs – scaling new heights together!</b>
8/06/01	<b>Physical Synthesis: The Key to Fast Timing Closure</b> <i>by Justine Chen, Software Product Marketing Manager, Xilinx Inc.</i>
7/23/01	<b>Real RapidIO Core Enables Terabit Networks</b> <i>by Abhijit Athavale, Solution Marketing Manager, Xilinx Inc.</i>
6/25/01	<b>New MicroBlaze High-performance Processor</b> <i>by Jim Burnham, MicroBlaze Product Marketing Manager, Xilinx Inc.</i>
4/30/01	<b>Design "Runtime" is the Key to Your Success</b> <i>by Craig Willert, Product Line Manager, Xilinx Inc.</i>

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