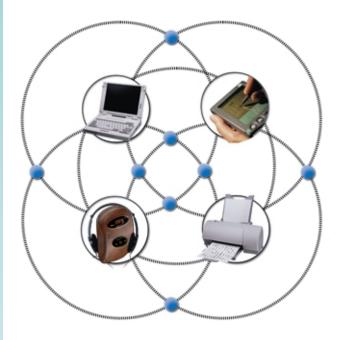


Developing Bluetooth-based products just got easier with the help of the Xilinx eSP (Emerging Standards and Protocols) Web portal, dedicated to meeting the challenges of developing products for the evolving Bluetooth specification. Here you will find a wealth of valuable information on design system partitioning, tutorials, proven reference designs, Xilinx solutions for Bluetooth systems, and a multitude of Bluetooth related resources and links, all aimed at simplifying the task of accelerating the design time of your next Bluetooth product.

As Bluetooth evolves,
Xilinx low-cost reprogrammable solutions, such as
Spartan™-II FPGAs and
CoolRunner™ CPLDs enable
you to keep your designs on
the leading edge. Xilinx
solutions combined with
eSP give you the competitive
advantage.

www.xilinx.com/esp

eSP – Meeting the Bluetooth Challenge



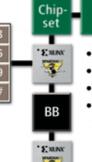
Xilinx Is the Solution for Your Next Bluetooth Design

Low-cost Spartan-II FPGAs and CoolRunner CPLDs, along with the eSP Web portal, are the perfect answer to your most difficult Bluetooth design problems, simply because they give you design flexibility that is hard to beat. The Bluetooth specification is still evolving, so trying to design a Bluetooth-based product is like trying to hit a moving target.

Xilinx can help resolve the following challenges in bringing your Bluetooth design to market:

- Integrating system components
- Implementing a host-optimized HCI bridging solution
- Adding unique features and value-added capabilities
- Accelerating your timeto-market
- Reducing support and maintenance costs with a field upgradeable solution
- Enabling designs to quickly address new opportunities

Xilinx Bluetooth Solutions



- RF-to-BB Integration
- HCI Bridging
- Peripheral Integration
- Application Enhancement and Acceleration



RF to Baseband Integration

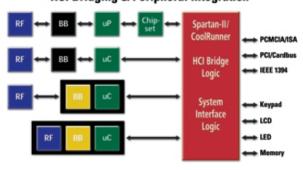
Xilinx low-cost FPGAs and CPLDs are the ideal solution for interfacing critical Bluetooth components such as RF chips and Baseband controllers. Xilinx solutions give you the added flexibility of using multiple RF vendors' chips, while at the same time giving you a low-cost, fast time-tomarket solution.

RF Radio/Baseband Integration The RF to Baseband interface is not standardized in the Bluetooth specification

HCI Bridging and Peripheral Integration

Whether you are configuring your design with peripherals such as keypads and PCI, or creating a host controller interface (HCI) to PCMCIA/ISA, Xilinx FPGAs and CPLDs are your system interface logic solution. With less than 100µA power consumption, CoolRunner CPLDs are ideally suited for low-power Bluetooth applications, providing flexibility and high performance in battery operated handheld applications.

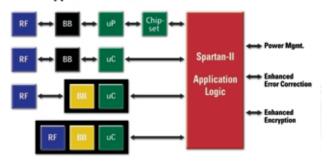
HCI Bridging & Peripheral Integration



Enhanced Capabilities and Future Innovation

Whether your Bluetooth product demands better power management or enhanced encryption technology, Xilinx FPGAs and CPLDs are the answer. Xilinx-based enhancements and extensions are an ideal way to differentiate your design while leveraging the low-cost components of the Bluetooth marketplace.

Application Acceleration/Enhancement



Xilinx is the Solution for You!

Visit the eSP Web portal, and let us show you just how easy it is for you to develop your next low-cost Bluetooth product at speeds you've never imagined possible. Xilinx has everything you need, including low-cost FPGAs and CPLDs, a wide array of Intellectual Property solutions, proven reference designs and development boards, software, and design services. The complete Bluetooth solution that provides you with flexibility, shorter development cycles, and cost competitiveness is simply Xilinx!

Visit the eSP website at:

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