FP-MCU Family Errata Sheet

February, 2008, Ver 0.5 **Errata Sheet, ES060**

Introduction

This document describes the errata information on the FP-MCU AG1F1 and AG1F4 devices. Details include known issues, trigger conditions, affected devices, and available workaround. This errata sheet should be used to compare to the datasheets for these devices to fully describe the device functionalities.

Device Identification

These errata apply to the FP-MCU family devices. The device revision is identified by the Revision ID on the top-side marking. **Figure 1** shows a FP-MCU device's top-side marking. The other devices are marked similarly.

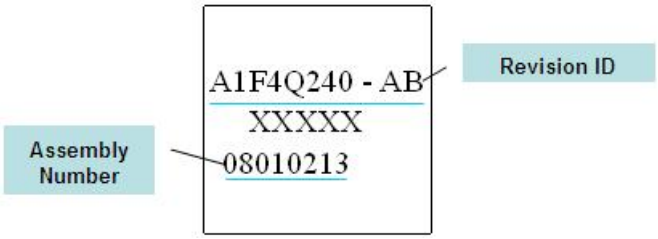

Figure 1 FP-MCU Device Top-side Marking

Errata Summary

Table 1 summarizes the known issues and which device revision each issue affects. See **Errata Details** for a detailed description of each known issue.

Table 1 FP-MCU Family Issues

Issue	Affected Device (Revision ID)
SPIO Issue at 3.3V Power Supply	AG1F1T144-ES AA, A1F4Q240-AB
IO Issue during Configuration	AG1F1T144-ES AA
DA Instruction Error	AG1F1T144-ES AA

Errata Details

This section provides the detailed errata information on each known issue.

1. SPIO Issue at 3.3V Power Supply

Problem Introduction:

SPIO cannot work sustained and stable at the power voltage of 3.3V in AG1F1T144-ES AA and A1F4Q240-AB devices.

Trigger Conditions:

This problem occurs only when the SPIOs work at 3.3V power supply.

Workaround:

Use 2.5V as the power supply for SPIOs.

2. IO Issue during Configuration

Problem Introduction:

During configuration mode, some IOs have signal output instead of keeping at a high-impedance state in AG1F1T144-ES AA devices.

Trigger Conditions:

This problem occurs to IOs listed in **Table 2** during the configuration mode.

Table 2 Affected IOs

IO77	IO82	IO89	IO94	IO101	IO106
IO78	IO85	IO90	IO95	IO102	IO107
IO79	IO86	IO91	IO96	IO103	IO108
IO80	IO87	IO92	IO97	IO104	IO130
IO81	IO88	IO93	IO100	IO105	IO131

Workaround:

Initial the peripheral equipment/devices after the successfully configuration if you use IOs in **Table 2**.

3. DA Instruction Error

Problem Introduction:

The “DA A” instruction causes error when implementing DA instruction in 8051 IP of AG1F1T144-ES AA devices at the condition of A [7:4] ==9 and A [3:0] > 9.

Trigger Conditions:

This problem occurs only when A [7:4] ==9 and A [3:0] >9.

Workaround:

Use the transfer tool **datrans.exe** provided by Agate Logic to modify the DA instruction. You can get the transfer tool from your Agate CD-ROM.

Supplemental Information

For questions about these errata, please go to www.agatelogic.com/Technology Support or contact your Agate Logic sales representative.

For the most up-to-date information about this errata sheet, please register on the Agate Logic website at www.agatelogic.com.cn, and check for the most recent release.

This errata sheet applies to the following documents:

FP-MCU AG1F1 Device Datasheet
www.agatelogic.com.cn
FP-MCU AG1F4 Device Datasheet
www.agatelogic.com.cn

Revision History

This section shows the revision history for this document.

Date	Version	Revision
February 18, 2008.	0.5	Initial Agate Logic Release