
PLD Software Tools Overview

Atmel's philosophy is that you should be able to use standard tools to design with our programmable logic devices.

With the tools that Atmel has available, we can serve the needs of beginning users as well as more experienced users. Based on the background of the user, we can make recommendations on the most cost-effective solution (see Table 1). If you have any questions regarding which package is best suited to a particular set of needs, please contact the Atmel PLD Technical Support Hotline at (408) 436-4333 or e-mail to pld@atmel.com.

This document outlines general system configurations required to use the various systems, details each package including ordering information, and includes a section with suggested systems for various types of users.

Atmel offers several levels of systems to meet the various needs of our customers. The systems are described on the following pages, along with a brief description of their function. For further information, contact your local Atmel sales representative, or the Atmel PLD Hotline at (408) 436-4333, or PLD e-mail at pld@atmel.com.



CMOS PLD Development Software Support Overview

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Table 1. PLD Software System Recommendations

User	Experience	Recommended System
New PLD User	No prior PLD experience. Wants basic support for PAL-type and V-Series devices and is willing to do manual pin assignment.	ATDS1000PC (Atmel-WINCUP), ATDS1100PC (Atmel-Synario)
PAL-type Device User	Knows ABEL, CUPL, or PALASM, and is familiar with 22V10 design. Wants to move up in density, and wants automatic fitting to reduce need to learn detailed internals of each device.	ATDS1100PC
Complex PLD User or TTL Designer	Uses complex PLDs from Altera, XILINX, or Lattice. Wants state-of-the-art tools for Atmel's CPLDs to take advantage of their speed and density. Wants schematic capture, simulation, and multiple design entry modes. Does not need full timing simulation.	ATDS1100PC (Atmel-Synario)
	Device Simulation	ATDS1120PC (Atmel-Synario Verilog Timing Simulation option) ATDS1140PC (Atmel-Synario VHDL Simulation option)
	Wants VHDL Entry Support	ATDS1130PC (Atmel-Synario VHDL option)
Atmel-ABEL/ABEL/Viewlogic User	Uses Atmel-ABEL version 5.1 or lower, Data I/O ABEL 5.1 or lower, or Viewlogic's Workview/ProPLD	Upgrade to ATDS1100PC

Note: 1. For proper ordering codes, consult the Atmel PLD Software Tools Product Description List.

Product	Description
ATDS1000PC	Atmel-WINCUP
ATDS1100PC	Synario-basic Schematic Capture, ABEL, Functional Simulation
ATDS1120PC	Synario-Verilog Timing Simulation
ATDS1130PC	Synario-VHDL Synthesis
ATDS1140PC	Synario-VHDL Simulation

Table 2. PLD System Configuration Requirements

		ATDS1000PC (Atmel-WINCUP)	ATDS1100PC (Atmel-Synario)
Platform		PC 486/Pentium	Pentium
Operating System		Windows 95, Windows 98	Windows 95/98/NT
Display Adaptor		CGA, EGA, VGA or SVGA	VGA/SVGA
System Memory		8 MB	16 MB
Hard Disk	Space Recommended	10 MB	60 MB
I/O Ports Required		Not required	1 parallel (for security block) 1 serial (for mouse)
Mouse		Not required	2/3-button mouse



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