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FOR IMMEDIATE RELEASE

**XILINX DELIVERS FIRST COMPLETE, STANDARDS-BASED,
SHRINK-WRAPPED SOFTWARE FOR PROGRAMMABLE LOGIC**

*New Xilinx Foundation Software Series Removes Barriers for Entry Level Users
to Design with HDLs and Easily Migrate to Higher Density Solutions*

SAN JOSE, Calif., April 1, 1996—Xilinx, Inc., (NASDAQ:XLNX) today announced the Xilinx Foundation series, a fully integrated set of software solutions that support a broad range of CPLD and FPGA design requirements. The easy-to-use, Windows-based software provides access to industry standard Hardware Description Languages (HDLs), synthesis, schematic entry, gate level simulation, and the Xilinx XACT^{step} implementation tools. The Xilinx Foundation series allows users to shorten design cycles and the learning curve associated with designing with industry standard HDLs.

“Today’s designers need easy-to-use, technology-independent software solutions to support the rapid innovation of new PLD families without having to learn new design methodologies,” said Kenn Perry, Xilinx director of software marketing. “The Xilinx Foundation series provides an entire suite of standards-based design tools leveraging an easy-to-learn VHDL design environment.”

Integrated, Standards-Based Features

Since the Xilinx Foundation tools are integrated into a common design management environment, users have access to all technology from design entry to implementation to verification. As a result, the new series provides ease and flexibility when designing with industry standard languages. For example, an integrated, push-button HDL editor provides the user immediate HDL assistance

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during mixed design entry. Among its variety of world-class features, the new software series includes the following:

- **HDL Wizard** makes language-based design easy and efficient in both VHDL and ABEL-HDL. It includes an HDL editor with built-in intelligence for learning HDL, language templates for cutting and pasting of common HDL functions, and color coding of HDL key words for leading the user through the text entry process.

- **VHDL Synthesis** provides a simple, low cost tool for synthesizing VHDL text files and supports fully compliant IEEE 1076 code. Proven compile times show a designer can synthesize a 2,000-3,000 gate design in two minutes. Design files are non-proprietary, technology-independent, and Synopsys-compatible. As a result, designs created with VHDL can be reused even if the target device changes.

- **ABEL-HDL Design** provides easy migration from PAL designs to higher density Xilinx CPLDs and FPGAs. It includes push-button design flow with hierarchical design entry and JEDEC file conversion, enabling reuse of existing PAL codes. A fitter architecture with automatic device selection allows designers to focus on design functionality.

“Xilinx’ business strategy with its new series of programmable logic design tools is an industry breakthrough,” said Rita Glover, president and principal analyst of EDA Today, Phoenix, Arizona. “The openness of the Xilinx Foundation series breaks down the barriers that have previously held customers captive to particular silicon architectures. Users will welcome the ability to do CPLD and FPGA design in a standards-compliant, technology-independent environment that is also low cost and easy to use.”

Pricing and Availability

The Xilinx Foundation software solutions are available now on PC platforms running Microsoft Windows 3.1. Availability on Microsoft Windows ‘95 and Windows NT platforms will be announced later this year.

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XILINX FOUNDATION SERIES

Product	Includes	Price	Family/Device Support
Foundation XABEL-CPLD	Simulation, ABEL-HDL design support, Xilinx CPLD implementation tools	\$495	XC7300 family XC9500 family
Foundation BASE	Schematic, simulation, ABEL-HDL design support, Xilinx CPLD and FPGA implementation tools	\$995	XC7300 family XC3120A/3130A/3142A XC3020A/3030A/3042A XC4003E XC5202/5204
Foundation BASE VHDL	VHDL synthesis, schematic, simulation, ABEL-HDL design support, Xilinx CPLD and FPGA implementation tools	\$1,495	XC7300 family XC3120A/3130A/3142A XC3020A/3030A/3042A XC4003E XC5202/5204
Foundation STANDARD	Schematic, simulation, ABEL-HDL design support, XBLOX, floorplanner, Xilinx CPLD and FPGA implementation tools, <i>support for up to 20,000 gates</i>	\$4,995	XC3100A family XC3000A family XC4000E family XC5200 family XC7300 family
Foundation STANDARD VHDL	VHDL synthesis, schematic, simulation, ABEL-HDL design support, XBLOX, floorplanner, Xilinx CPLD and FPGA implementation tools, <i>support for up to 20,000 gates</i>	\$5,995	XC3100A family XC3000A family XC4000E family XC5200 family XC7300 family

Founded in 1984, Xilinx is the world's largest supplier of programmable logic solutions comprising industry leading device architectures and world class design software. Headquartered in San Jose, Calif., the company pioneered the market for field programmable gate array (FPGA) semiconductor devices that provide high integration and quick time-to-market for electronic equipment manufacturers in the computer, peripheral, telecommunications, networking, industrial control, instrumentation, and high reliability/military markets.

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Note to editors: For more information on Xilinx, access our World Wide Web site at "<http://www.xilinx.com>". Xilinx is a registered trademark of Xilinx, Inc. All XC-prefix product designations, Xilinx Foundation, Xilinx HDL Wizard and XACTstep are all trademarks of Xilinx, Inc. Other brands or product names are trademarks or registered trademarks of their respective owners.

To the extent that matters covered in this release are forward looking statements, they involve risks and uncertainties, including timely availability and acceptance of new products, business conditions, the growth of the programmable logic market, and other risks that are described in the company's SEC filings, including the Form 10-Q for the quarter ended December 31, 1995.

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