

Xilinx Design Series Text Books



Used by over 150,000 students per year in colleges and universities worldwide, Xilinx programmable logic devices are more than twice as popular as those of our nearest competitor

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Xilinx, along with Prentice Hall, the premier publisher of engineering books and software for the academic market, recently announced the publication of a new engineering text that includes a copy of the Xilinx Student Edition software. The text, *Modeling Synthesis and Rapid Prototyping with the Verilog HDL*, by Michael D. Ciletti, is the first textbook to be offered as a package with the popular Xilinx programmable logic tools. Xilinx and Prentice Hall have been partners for two years, providing the Xilinx Student Edition software for college-level instruction.

Ciletti's text on Verilog synthesis and rapid prototyping is designed for the undergraduate computer science, computer engineering, and electrical engineering courses in digital design and hardware description languages. It provides in-depth instruction combined with the sophisticated tools students will need to put those theories into practice in the laboratory environment. This offering builds on the success of the Xilinx Student Edition software and furthers the Xilinx commitment to our university program.

"Prentice Hall is pleased to offer a logic design simulator that complements the content of Michael Ciletti's textbook," commented Tom Robbins, publisher for Prentice Hall. "The Xilinx Student Edition allows students to put into immediate practice the tools and techniques they learn from the text right on their own computer."

Other engineering texts in the Xilinx Design Series published by Prentice Hall will also

include the Xilinx Student Edition software:

- The Xilinx Practical Designer by Dave Van Den Bout.
- VHDL: Starter's Guide From Simulation to Synthesis, by Sudhakar Yalamanchili.
- Digital Design: Principles and Practice (Third Edition), by John F. Wakerly.
- Other titles, to be announced.

Instructors who wish to make the Xilinx Student Edition software part of their curriculum have the option of packaging the Student Edition lab book with any Prentice Hall digital design book for additional savings and course completeness. For more information, see: www.xilinx.com/programs/xds1.htm.

The *Modeling Synthesis and Rapid Prototyping with the Verilog HDL* text packaged with the Xilinx Student Edition software is now available for a list price of under \$100. A special website dedicated to the lab exercises will always have the latest up-to-date files.

About Prentice Hall

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