

# Get a Head Start with a New Virtex Development Board

**To demonstrate the versatility of the new Virtex FPGAs, Avnet Design Services has created a new development tool—the Virtex Development System.**

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**B**uilt by the engineers in Avnet Design Services' Technical Service Center, the Virtex Development System is a circuit board that gives you a significant advantage—it helps you reduce your development cycle and allows you to immediately develop and test your Virtex-based designs. The board is part of Avnet Design Services' ongoing series of reference designs and development systems that help you use the latest components without a prohibitive learning curve. In addition to the development board, the Virtex Development System comes bundled with either the Foundation Series or Alliance Series development software from Xilinx and includes a Xilinx fitter cable and documentation.

The Virtex Development System is available exclusively from Avnet Electronics Marketing and features on-board peripherals, which:

- Demonstrate the high performance of Virtex FPGAs.
- Use various FPGA configuration modes.
- Interface to the Xilinx download cable.
- Demonstrate multiple system clocks and delay locked loops (DLLs)
- Demonstrate various memory interfaces.
- Demonstrate core support and functionality.
- Provide a develop-

ment/reference platform for developers.

The Virtex Development System is available in the following configurations:

- Alliance Series: DS-ALI-STD-PC-SK/B  
RESALE \$1195.00 USD
- Foundation Series: DS-FND-EXP-PC-SK/B  
RESALE \$2795.00 USD

To order the Virtex Development System, visit the Avnet Electronics Marketing Semiconductor website at: [www.em.avnet.com/semis/ads/virtex.html](http://www.em.avnet.com/semis/ads/virtex.html).

The Virtex Development System board (Figure 1) provides maximum flexibility by using the XCV300BG432, which is surrounded by popular emerging technologies. The board includes physical layer interface chips, memory interfaces, bus interfaces, a Xilinx JTAG download cable interface, and a RAMDAC interface for CRT display support. With the supporting

electronics in place and electrical interfaces defined, the function of the Virtex Development System depends on the FPGA logic and cores that you implement. You can use board in a 3.3V PCI slot, or operate it stand-alone. 

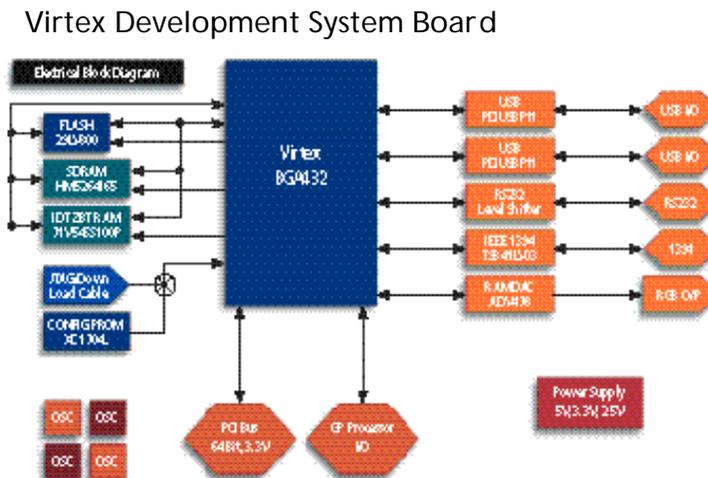


Figure 1