

The New XC95144



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The FastFLASH family of CPLDs just got better with the recent introduction of the XC95144. This newest member of the XC9500 family completes the fastest growing line of CPLDs in the industry. The XC95144 features 144 macrocells with 7.5 ns pin-to-pin delays and is offered in 100-pin TQFP, 100-pin PQFP, and 160-pin PQFP packages.

The XC95144 is the first CPLD to use the new advanced FastFLASH process from United Semiconductor Corporation in Taiwan. This new 0.5-micron process technology offers up to a 50% die size reduction from the previous 0.6-micron

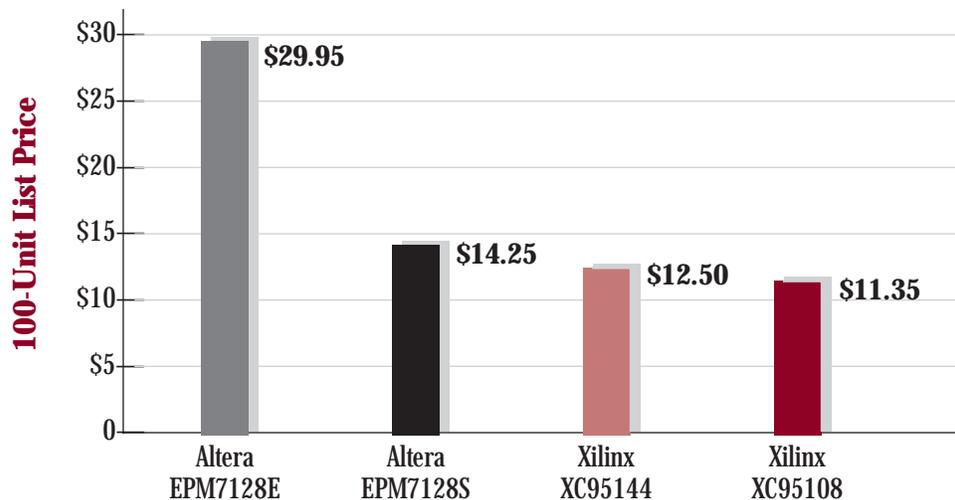
process. The rest of the XC9500 family will be transitioned over to the new process by the end of 1998.

Higher Density at a Lower Price

The 50% die size reduction enables Xilinx to maintain its CPLD price leadership into the future. As you can see, the XC95144 offers 12% more capacity than the competitors' conventional 128-macrocell device at a 62% lower price (see chart below).

The new XC95144 is a superior CPLD solution at a rock bottom price. ♦

12% More Headroom Than Competitors' Devices!



Source: Arrow Electronics Web Page, 6/15/98; Xilinx May 1998 Price Book