

# XC6264 Added to XC6200 Family of RPUs

Xilinx recently announced the addition of the 64,000-gate XC6264 device to the XC6200 family of reconfigurable processing units (RPUs). The XC6264 and XC6216 RPUs are the only programmable logic devices that have been optimized for reconfigurable logic applications such as real-time adaptive filtering and algorithm acceleration.

While FPGAs traditionally have been used to implement individual logic designs, RPUs are intended to support multiple designs in a single piece of silicon — either by “swapping

out” unnecessary pieces of a design to save silicon space or by changing logic on-the-fly to increase performance.

Innovative XC6200 features such as fast partial reconfiguration, a built-in microprocessor interface and an open bitstream format make RPUs the component of choice for system designers exploring or using dynamic hardware designs.

***For more information on the XC6200 RPU family, visit WebLINX at [www.xilinx.com](http://www.xilinx.com). ♦***