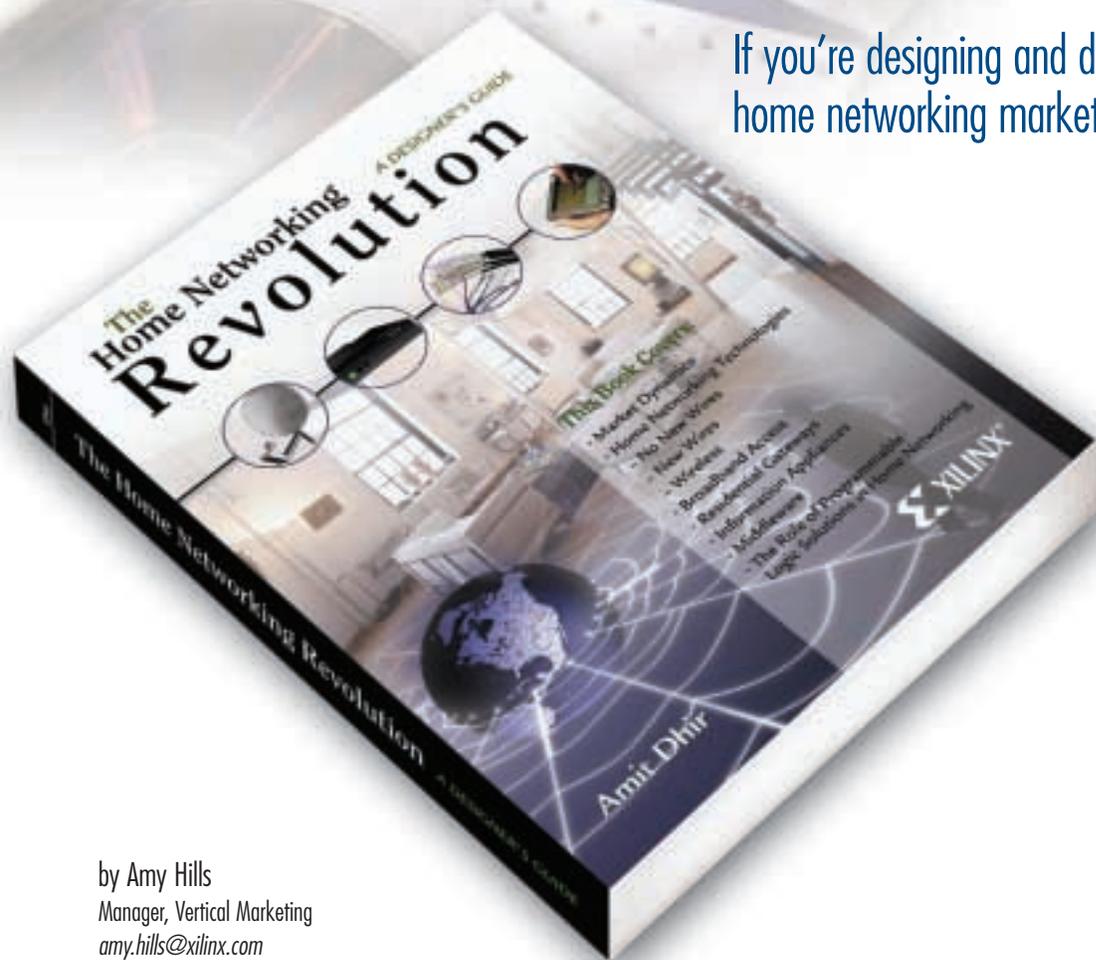


# Xilinx Publishes Free Comprehensive Guide to Home Networking

If you're designing and developing products for the home networking market, this book is a must-read.



WITHOUT A DOUBT, THIS IS THE MOST THOROUGH PIECE WRITTEN ON THE BURGEONING HOME NETWORKING INDUSTRY. THE CONTENT AND FORMAT IS SUITABLE FOR A DIVERSE AUDIENCE INCLUDING ENGINEERS, INVESTORS, AND HIGH-TECH AFICIONADOS. IT IS A MUST-READ FOR ANY CONSTITUENT OF THE HOME NETWORKING INDUSTRY.

-TIM MAHON,  
DIRECTOR, CREDIT SUISSE FIRST BOSTON

by Amy Hills  
Manager, Vertical Marketing  
amy.hills@xilinx.com

In our continuing commitment to help our customers accelerate the design and development of home networking products, Xilinx has published a comprehensive guide called *The Home Networking Revolution, A Designer's Guide*. The 259-page book explores digital convergence and home networking dynamics, and it provides a thorough overview of the key technologies and components.

Written by Amit Dhir, manager of Strategic Solutions Marketing, and edited by Tom Pyles, editor of Corporate Technical Publications, the guide covers topics ranging from broadband access tech-

nologies to residential gateways to home networking options. Through tutorials on current and emerging standards and protocols (eSP), system block diagrams, and reference designs, the guide will help you perfect the design and development of consumer products. You will also find information on how programmable logic solutions can accelerate your time to market of home networking products.

You can order your free copy of *The Home Networking Revolution, A Designer's Guide* by registering on the Xilinx eSP Web portal at [www.xilinx.com/esp](http://www.xilinx.com/esp).

Through *The Home Networking Revolution, A Designer's Guide*, in conjunction with the eSP Web portal, you will find that Xilinx provides you with the resources you need to:

- Build best-in-class products
- Get your product to market far ahead of the competition
- Extend the life of your product with Internet Reconfigurable Logic.