

Xcell Journal Online



Q4-2001 Archive

Start Here Page

Date	Article Description	Issue No.
11/19/01	View <small>from the top</small> Managing in Difficult Times <i>by Wim Roelandts, CEO Xilinx</i>	41
11/19/01	Xilinx ISE 4.1i Delivers the Speed You Need <i>by Lee Hansen, Software Product Marketing Manager, Xilinx Inc.</i>	41
11/19/01	Leveraging ASIC Expertise for Platform FPGAs <i>by Sanjiv Kaul, Sr. VP and General Manager, Synopsys, Inc.</i>	41
11/19/01	New Spartan-IIe FPGA Family <i>by Ashok Chotai, Senior Competitive Marketing Engineer, Xilinx Inc.</i>	41
11/19/01	Reconfigurable Vehicles Are Just Around the Corner <i>by Karen Parnell -- European Marketing Manager High Volume Products, Xilinx Inc.</i>	41
11/19/01	Improve Your Time to Market with Online Solutions from Xilinx <i>by Kristi Gloppen, Manager, eBusiness Marketing, Xilinx, Inc.</i>	41
11/19/01	High-Performance Platform FPGAs Now Need Formal Verification <i>by Bassilios C. Petrakis, Business Development Manager, Verplex Systems, Inc.</i>	41
11/19/01	Two Virtex-II FPGAs Deliver Fastest, Cheapest, Best High-Performance Image Processing System <i>by Tom Dillon, President, Dillon Engineering Inc.</i>	41
11/19/01	Synopsys and Xilinx Unveil Next Generation Flow for Platform FPGAs <i>by Jackie Patterson, Director of Marketing Programs, Synopsys Inc.</i>	41
11/19/01	Synplicity Announces TOPS -- A Second-Generation Physical Synthesis Technology for Xilinx FPGAs <i>by Jeff Garrison, Director of Marketing, FPGA Products, Synplicity, Inc.</i>	41
11/19/01	Speed Up Verification of Long Transaction Sequences with MicroBlaze Soft Processors <i>by Warren Miller, VP of Marketing, Avnet Design Services</i>	41
11/19/01	Co-Processor Acceleration and Design Reuse: Extending the Market for Platform FPGAs with DK1 Design Suite <i>by Dennis Nye, Senior Vice President, Worldwide Sales and Marketing, Celoxica Limited</i>	41
11/19/01	Giga-Sample DSP Board Using Virtex-E FPGAs <i>by Dr. Stephen King, Business Development Manager, Central Research Laboratories Limited</i>	41
11/19/01	Add Internet Connectivity with Spartan-II FPGAs and the UDP Stack Core <i>by Jim Beneke, Director Technical Marketing, Insight Electronics</i>	41
11/19/01	Internet Reconfigurable Logic with Alpha Data's ADM-XRC <i>by Tomas Whitlock, Design Engineer Alpha Data Parallel Systems Ltd.</i>	41
11/19/01	WebFITTER Gets a Face-Lift <i>by Larry McKeogh, CPLD Software Marketing Manager, Xilinx, Inc.</i>	41
11/19/01	Xilinx Timing Analyzer Is Default Viewer for Static Timing Reports in ISE 4.1i <i>by Kate Meilicke, Technical Marketing Engineer, Xilinx, Inc.</i>	41
11/19/01	Speed Up Your Design Verification <i>by Ricky Escoto, Manager, Product Line Marketing, Xilinx, Inc.</i>	41
11/19/01	ProActive Timing Closure Delivers up to 133% Better Device Performance	41

	<i>by Lee Hansen, Product Marketing Manager, Xilinx, Inc.</i>	
11/19/01	Understanding Physical Synthesis and Timing Closure <i>by Hamid Agah, Technical Marketing Manager, Xilinx, Inc.</i>	41
11/19/01	Xilinx Takes Power Analysis to New Levels with XPower <i>by Steve Wenande, Senior Technical Marketing Engineer, Xilinx, Inc.</i>	41
11/19/01	LeonardoSpectrum Now Supported in the Xilinx ISE Software <i>by Steve Wenande, Senior Technical Marketing Engineer, Xilinx, Inc.</i>	41
11/19/01	New Flexbus-4 Core <i>by Paul Morrison, System Logic and Networking Engineers, Xilinx, Inc.</i>	41
11/19/01	Networking Comes Home <i>by Robert Bielby, Senior Director, Strategic Solutions, Xilinx Inc.</i>	41
11/19/01	Programmable Logic Will Drive Growth in Broadband Access and Home Networking <i>by Robert Bielby, Senior Director, Strategic Solutions, Xilinx Inc.</i>	41
11/19/01	Home Networking -- Integrating Information Appliances with Personal Computers <i>by Amit Dhir, Manager, Strategic Solutions, Xilinx Inc. by Krishna Rangasayee, Senior Manager, Strategic Solutions, Xilinx Inc.</i>	41
11/19/01	Bluetooth Wireless Technology and Personal Area Networking In Your Home and on the Road <i>by Krishna Rangasayee, Senior Manager, Strategic Solutions, Xilinx Inc. by Mike Nelson, Senior Manager, Strategic Solutions, Xilinx Inc.</i>	41
11/19/01	FPGAs Enable Wireless LANs <i>by Amit Dhir, Manager, Strategic Applications</i>	41
11/19/01	Residential Gateways <i>by Amit Dhir, Manager, Strategic Solutions, Xilinx Inc.</i>	41
11/19/01	Xilinx Publishes Free Comprehensive Guide to Home Networking <i>by Amy Hills, Manager, Vertical Marketing, Xilinx</i>	41
11/19/01	iMPACT - New Configuration and Programming Software <i>by Frank Toth, Marketing Manager, Configuration Solutions, Xilinx, Inc.</i>	41
11/19/01	Platform FPGA SystemIO Solution <i>by Peggy Abusaidi, Product Marketing Manager, Xilinx, Inc.</i>	41
11/19/01	Xilinx Launches Signal Integrity Website <i>by Rob Schreck, Senior Marketing Manager, IC Solutions Marketing, Xilinx, Inc.</i>	41
11/19/01	And the Winner is... Cool Module Design Contest. <i>by Steve Prokosch, Product Marketing, Xilinx, Inc.</i>	41
11/19/01	Giga-Sample DSP Board Using Virtex-E FPGAs <i>by Dr. Stephen King, Business Development Manager, Central Research Laboratories Limited</i>	41
11/19/01	Internet Reconfigurable Logic with Alpha Data's ADM-XRC <i>by Tomas Whitlock, Design Engineer Alpha Data Parallel Systems Ltd.</i>	41
11/19/01	Virtex-II DSP Engines Enable Software Defined Radio <i>by Katie DaCosta, DSP Solutions Marketing, Xilinx, Inc.</i>	41
11/19/01	Improve Your Time to Market with Online Solutions from Xilinx <i>by Kristi Gloppen, Manager, eBusiness Marketing, Xilinx, Inc.</i>	41
11/19/01	The Terabit Network Forum <i>by Abhijit Athavale, System Interface Solutions Marketing Manager, Xilinx, Inc.</i>	41