

# Xilinx and AcceLight Deliver High-Performance Photonic Solutions

The industry's fastest FPGAs are the key enabler for photonic service switches.

by Xilinx Staff

Xilinx recently announced its collaboration with AcceLight Networks™ – a leading aggregation switching and cross-connect company – for the delivery of AcceLight's PXS™ 540 family of photonic service switches (PSS). AcceLight's unique architecture, enabled by the Xilinx high-density, high-speed Virtex™-II Platform FPGAs, delivers an integrated system for SONET/SDH STS-1 grooming, optical path, and MPLS label switching from 80 gigibits to 1.28 terabits in capacity. Today's service providers can benefit from the agile service configura-

**"The advanced architecture and flexibility of the Xilinx Platform FPGAs helped us to shorten our development cycle and achieve the breakthrough performance offered in our PXS 540.**

**FPGAs are key to rapidly delivering the emerging technologies needed to build a flexible multiservices system that allows service providers to drive their costs down by up to 70%."**

– Paul Chow, director of ASIC technology at AcceLight Networks

tions of the PXS 540 to deliver inter-city TDM, optical, and MPLS switched services for the wavelength core.

AcceLight Networks is among a growing number of companies using Xilinx FPGAs to gain a competitive advantage. AcceLight™ selected Xilinx FPGAs over competitive devices based upon the unprecedented logic, memory, and I/O capacity of the Virtex-II line.

"AcceLight's innovative application of Xilinx FPGAs represents our ability to make a significant impact with enabling technologies for our customers," said Robert Bielby, senior director of Strategic Solutions at Xilinx. "AcceLight's patented architecture will have a significant impact in reducing operational costs for global service providers while providing a more efficient network."

#### About Virtex-II FPGAs

The Xilinx Virtex-II Platform FPGA family delivers the highest performance and highest density of any programmable logic solution available. The innovative Virtex-II IP-

Immersion architecture enables integration of both hard and soft intellectual property (IP), enhanced system memory, and lightning-fast DSP performance. It provides the best platform for advanced digital designs in the industry. With densities ranging from 40,000 to 8,000,000 system gates, Virtex-II solutions are empowered by advanced design tools that reduce development time through fast design, powerful synthesis,

smart implementation algorithms, and efficient verification capabilities.

#### eSP for Optical Networking and Metro Area Design

Xilinx eSP Web portal ([www.xilinx.com/esp](http://www.xilinx.com/esp)) is a proven resource for engineers. The latest segment on eSP is dedicated to accelerating the development of optical networking products. The site is a comprehensive resource, delivering a powerful array of solutions and information in a single location. ❧

#### About AcceLight Networks

AcceLight Networks, Inc. is a privately held, new-generation designer and manufacturer of the industry leading PXS 540 photonic service switch. The PXS 540 is an emerging network element that consolidates a B-DACS, optical cross connect, and MPLS switch into one multi-chassis system. With integrated IP routing and common G-MPLS connection management, the PXS 540 delivers inter-city switched services for the wavelength core. The company is headquartered in Bridgeville, Penn., and has extensive R&D facilities in Ottawa, Canada. For more information, visit [www.accelight.com](http://www.accelight.com).



*AcceLight Networks, AcceLight, and PXS are trademarks of AcceLight Networks, Inc.*