

# Upgrade to the Fastest Performance Ever.

[www.xilinx.com/ise4](http://www.xilinx.com/ise4)



The ISE Development Systems Deliver  
All the Speed You Need ...

- Ultra-Fast Run Times
- Superior Device Performance
- Increased Productivity

## Xilinx High-Performance Development Systems

The ISE design environment brings you the fastest and easiest family of design tools you can get. ISE combines advanced technologies such as Proactive Timing Closure with a flexible, easy-to-use graphical interface to help you achieve the best possible designs with the least time and effort, regardless of your experience level.

### Four Easy-to-Use Configurations

The Integrated Software Environment (ISE) development systems are available in the following configurations:

#### ISE WebPACK

ISE WebPACK™ is the easiest development system to get — it's free and it's on the Web! ISE WebPACK combines support for advanced HDL entry, synthesis, and verification capabilities for both CPLDs and select FPGAs. (Refer to the chart on the back)

#### ISE BaseX

ISE BaseX is the industry's most cost-effective, PC-based programmable logic design environment. The ISE BaseX configuration provides all of the capabilities contained within ISE WebPACK plus additional tools that will help you complete your programmable logic design even faster. (Refer to the chart on the back)

#### ISE Alliance

ISE Alliance is designed to fit into your existing design environment. The tools work seamlessly with those of our EDA partners.

#### ISE Foundation

ISE Foundation™ is a complete, ready-to-use design environment that integrates schematic, synthesis, and verification technology into an intuitive, yet highly advanced design solution.

### Xilinx also offers these optional productivity tools:

#### ModelSim Xilinx Edition II (MXE II)

Provides a complete HDL simulation environment.

#### ChipScope™

Enables in-system logic debugging and design verification.

#### Modular Design

Allows you to divide multi-million gate designs into smaller modules to increase your productivity.

#### Xilinx System Generator for Simulink®

Allows you to develop high performance DSP systems for Xilinx FPGAs.

### See For Yourself

Upgrade now and take advantage of the fastest and easiest development environment in the industry — ISE will save you time and effort in your next design. To get the full details on ISE, go to [www.xilinx.com/ise4](http://www.xilinx.com/ise4).

For information on Xilinx Embedded Software Design Tools, go to [www.xilinx.com/ise/embedded](http://www.xilinx.com/ise/embedded).

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## Development Systems Feature Comparison Guide

Feature	ISE WebPACK	ISE BaseX	ISE Foundation	ISE Alliance	
<b>Device Support</b>	Virtex™ Series	Up to 300K (Virtex-E and Virtex-II)	Up to 300K	ALL	ALL
	Spartan™ II/IIe Families	ALL	ALL	ALL	ALL
	Spartan™ /XL Families	No	Implementation Only*	Implementation Only*	ALL
	XC4000™ Series	No	Implementation Only*	Implementation Only*	ALL
	CoolRunner XPLA3 / CoolRunner™ II	ALL	ALL	ALL	ALL
	XC9500™ Series	ALL	ALL	ALL	ALL
<b>Design Planning</b>	Modular Design	No	Sold as an Option	Sold as an Option	Sold as an Option
	Educational Services	Yes	Yes	Yes	Yes
	Design Services	Sold as an Option	Sold as an Option	Sold as an Option	Sold as an Option
	Support Services	Web Only	Yes	Yes	Yes
<b>Design Entry</b>	Schematic Editor (Gate & Block Level HDL)	Yes	Yes	PC Only	No
	HDL Editor	Yes	Yes	Yes	Yes
	State Diagram Editor	Yes	Yes	PC Only	No
	CORE Generator	No	Yes	Yes	Yes
	Xilinx System Generator for Simulink	No	Sold as an Option	Sold as an Option	Sold as an Option
<b>Synthesis</b>	Xilinx Synthesis Technology (XST)	Yes	Yes	Yes	No
	Synplicity Synplify/Pro	No	Integration Kit	Integration Kit (PC Only)	Integration Kit (PC Only)
	Leonardo Spectrum	Integration Kit	Integration Kit	Integration Kit	Integration Kit
	Synopsys FPGA Compiler II	EDIF Only	EDIF Only	EDIF Only	EDIF Only
	ABEL	CPLD	CPLD	CPLD (PC Only)	No
<b>Implementation Tools</b>	iMPACT	Yes	Yes	Yes	Yes
	FloorPlanner	Yes	Yes	Yes	Yes
	Xilinx Constraints Editor	Yes	Yes	Yes	Yes
	Timing Driven Place & Route	Yes	Yes	Yes	Yes
	Timing Improvement Wizard	No	Yes	Yes	Yes
<b>Board Level Integration</b>	IBIS Models	Yes	Yes	Yes	Yes
	STAMP Models	Yes	Yes	Yes	Yes
	LMG SmartModels	Yes	Yes	Yes	Yes
	HSPICE Models**	Yes	Yes	Yes	Yes
<b>Verification</b>	HDL Bencher™	Yes	Yes	PC Only	No
	ModelSim Xilinx Edition (MXE II)	ModelSim XE II Starter***	ModelSim XE II Starter***	ModelSim XE II Starter***	ModelSim XE II Starter***
	Static Timing Analyzer	Yes	Yes	Yes	Yes
	ChipScope	No	Sold as an Option	Sold as an Option	Sold as an Option
	FPGA Editor with Probe	No	Yes	Yes	Yes
	ChipViewer	Yes	Yes	Yes	Yes
	XPower (Power Analysis)	Yes	Yes	Yes	Yes
	3rd Party Simulator Support	Yes	Yes	Yes	Yes
<b>IP/CORE</b>	For more information on the complete list of Xilinx IP products, visit the Xilinx IP Center at <a href="http://www.xilinx.com/ipcenter">http://www.xilinx.com/ipcenter</a>				

\*For Spartan, SpartanXL, and 4K device families, ISE supports the implementation by reading in gate-level EDIF netlist.

\*\*HSPICE Models available at the Xilinx Design Tools Site at [www.xilinx.com/ise](http://www.xilinx.com/ise).

\*\*\*MXE II supports the simulation of designs up to 1 million system gates and is sold as an option.

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100 BEST COMPANIES TO WORK FOR

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