



Xilinx is the Language of Design

The ISE design environment brings you the fastest, most complete family of design tools you can get. ISE combines advanced technologies such as Proactive Timing Closure with a flexible, easy-to-use graphical interface to help you achieve the best possible designs with the least time and effort, regardless of your experience level.

ISE Development Systems Deliver "All The Speed You Need."

With ISE, you can ...

- Take Advantage of the Fastest Programmable Logic Designs in the Industry
- Finish Faster and Implement Change Without Risk
- Increase Your Productivity and Reduce Cost
- Create Embedded Designs for Programmable Systems Using Virtex-II Pro™ Platform FPGAs

Optional Productivity Tools:

ModelSim Xilinx Edition II (MXE-II)

Offers a complete PC HDL simulation environment that enables you to verify the HDL source code as well as the functional and timing models of your designs.

ChipScope Pro

Enables in-system logic debugging and design verification. ChipScope™ Pro, combined with the Integrated Logic Analysis (ILA) core, allows real-time access to any node in the chip, with an easy-to-use GUI interface.

Modular Design

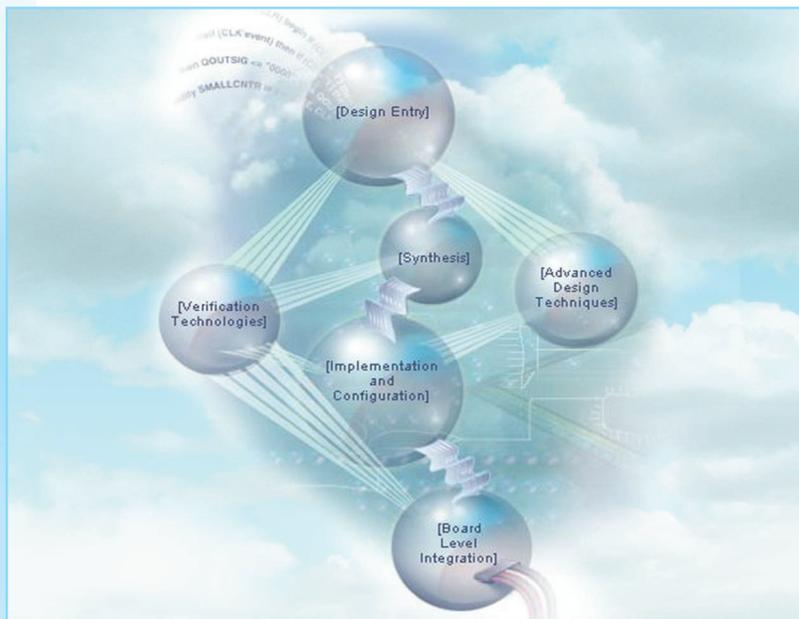
Provides a "divide and conquer" approach to multi-million gate designs, allowing you to focus on smaller modules, to increase your productivity.

Xilinx System Generator for DSP

Allows you to develop high-performance DSP systems for Xilinx FPGAs.

Embedded Software Design Tools

Provides the software design tools you need to create, edit, compile, link, load, and debug high level language code, usually C or C++, for execution on an embedded processor engine.



Four Easy-to-Use Configurations

The Integrated Software Environment (ISE) logic development systems are available in the following configurations:

ISE WebPACK

ISE WebPACK™ is the easiest development system to get — it's free and it's on the Web! ISE WebPACK allows you to complete your designs quickly with a combination of advanced HDL entry, synthesis, and verification capabilities for both CPLDs and select FPGAs.

ISE BaseX

ISE BaseX is the industry's most cost-effective, PC-based programmable logic design environment. It provides all of the capabilities contained within ISE WebPACK plus additional tools that will help you complete your programmable logic design even faster.

ISE Alliance

ISE Alliance is designed to fit into your existing design environment. It combines the high-performance Xilinx implementation tools with seamless integration with tools available from our EDA partners.

ISE Foundation

ISE Foundation™ is a complete, ready-to-use design environment that integrates schematic, synthesis, and verification technology into an intuitive, yet highly advanced design solution.

See For Yourself

Upgrade now and take advantage of the fastest and easiest development environment in the industry — ISE will save you time and effort in your next design. To get the full details on ISE, go to www.xilinx.com/ise.

ISE Feature Matrix

Feature	ISE WebPACK	ISE BaseX	ISE Foundation	ISE Alliance
Devices				
Virtex™ Series	Virtex-E: V50E – V300E Virtex-II: 2V40 – 2V250 Virtex-II Pro: 2VP2	Virtex: V50 – V300 Virtex-E: V50E – V300E Virtex-II: 2V40 – 2V250 Virtex-II Pro: 2VP2	ALL	ALL
Spartan™ II/IIe Families	ALL	ALL	ALL	ALL
CoolRunner™ XPLA3 / CoolRunner-II	ALL	ALL	ALL	ALL
XC9500™ Series	ALL	ALL	ALL	ALL
Services				
Educational Services	Yes	Yes	Yes	Yes
Design Services	Sold as an Option	Sold as an Option	Sold as an Option	Sold as an Option
Support Services	Web Only	Yes	Yes	Yes
Design Entry				
Schematic Editor	Yes	Yes	PC Only	No
HDL Editor	Yes	Yes	Yes	Yes
State Diagram Editor	Yes	Yes	PC Only	No
CORE Generator System	No	Yes	Yes	Yes
PACE (Pinout and Area Constraint Editor)	Yes	Yes	Yes	Yes
Architecture Wizards	Yes	Yes	Yes	Yes
DCM - Digital Clock Management				
MGT - Multi-Gigabit Transceivers				
3rd Party RTL Checker Support	Yes	Yes	Yes	Yes
Xilinx System Generator for DSP	No	Sold as an Option	Sold as an Option	Sold as an Option
Embedded System Design				
GNU Embedded Tools	No	No	Yes	Yes
GCC - GNU Compiler				
GDB - GNU Software Debugger				
WindRiver Xilinx Edition Development Tools	No	No	Sold as an Option	Sold as an Option
Diab C/C++ Compiler				
SingleStep Debugger				
visionPROBE II target connection				
Synthesis				
Xilinx Synthesis Technology (AST)	Yes	Yes	Yes	No
Synplicity Synplify/Pro	Integrated Interface	Integrated Interface	Integrated Interface (PC Only)	Integration Interface (PC Only)
Synplicity Amplify Physical Synthesis Support	Yes	Yes	Yes	Yes
Leonardo Spectrum	Integrated Interface	Integrated Interface	Integrated Interface	Integrated Interface
Synopsys FPGA Compiler II	EDIF Interface	EDIF Interface	EDIF Interface	EDIF Interface
ABEL	CPLD	CPLD	CPLD (PC Only)	No
Implementation				
iMPACT	Yes	Yes	Yes	Yes
FloorPlanner	Yes	Yes	Yes	Yes
Xilinx Constraints Editor	Yes	Yes	Yes	Yes
Timing Driven Place & Route	Yes	Yes	Yes	Yes
System ACE Configuration Manager	Yes	Yes	Yes	Yes
Modular Design	No	Sold as an Option	Sold as an Option	Sold as an Option
Timing Improvement Wizard	Yes	Yes	Yes	Yes
Board-Level Integration				
IBIS Models	Yes	Yes	Yes	Yes
STAMP Models	Yes	Yes	Yes	Yes
LMG SmartModels	Yes (Available from Synopsys)	Yes (Available from Synopsys)	Yes (Available from Synopsys)	Yes (Available from Synopsys)
HSPICE Models*	Yes	Yes	Yes	Yes
HDL Bench™	Yes	Yes	PC Only	No
Verification				
ModelSim™ Xilinx Edition (MXE II)	ModelSim XE II Starter**	ModelSim XE II Starter**	ModelSim XE II Starter**	ModelSim XE II Starter**
Static Timing Analyzer	Yes	Yes	Yes	Yes
ChipScope PRO	No	Sold as an Option	Sold as an Option	Sold as an Option
FPGA Editor with Probe	No	Yes	Yes	Yes
ChipViewer	Yes	Yes	Yes	Yes
XPower (Power Analysis)	Yes	Yes	Yes	Yes
3rd Party Equivalency Checking Support	Yes	Yes	Yes	Yes
SMARTModels for PPC and Rocket I/O	No	Yes	Yes	Yes
3rd Party Simulator Support	Yes	Yes	Yes	Yes
Platforms	PC Only	PC, Sun Solaris, Linux	PC, Sun Solaris, Linux	PC, Sun Solaris, Linux
IP/CORE	For more information on the complete list of Xilinx IP products, visit the Xilinx IP Center at http://www.xilinx.com/ipcenter			

*HSPICE Models available at the Xilinx Design Tools Center at www.xilinx.com/ise. **MXE II supports the simulation of designs up to 1 million system gates and is sold as an option. For more information, visit the **Xilinx Design Tools Center** at www.xilinx.com/ise

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