



Lower Project Costs with Fast and Easy Design Debugging and Verification

Your design debugging and verification cycle is often the critical bottleneck that slows your product completion. Simulation helps, but nothing substitutes for real-time debugging at system speeds. ASIC and traditional debugging solutions require you to dedicate critical logic space, come up with complex capture schemes, and allocate large numbers of I/O pins. These solutions provide limited access to signal activity inside the device.

ChipScope™ Pro from Xilinx delivers real-time debugging with shorter verification cycles and lower project costs. You can easily capture and view signal activity inside any of our leading-edge FPGAs. By inserting special low-impact IP debugging cores directly into your HDL code or design netlist, you can debug and verify FPGA logic, system bus, and internal processor bus activity, capturing signals at or near system operating speeds. And you can easily change your trace points without having to recompile your design.



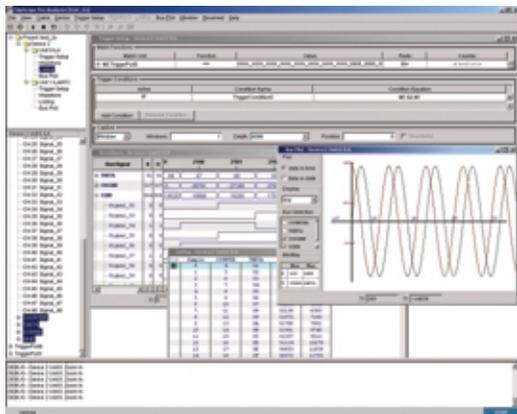
Debugging That Is Much Easier

With ChipScope Pro you have real-time system debugging and verification tools that far outperform other logic debugging solutions. ChipScope Pro gives you:

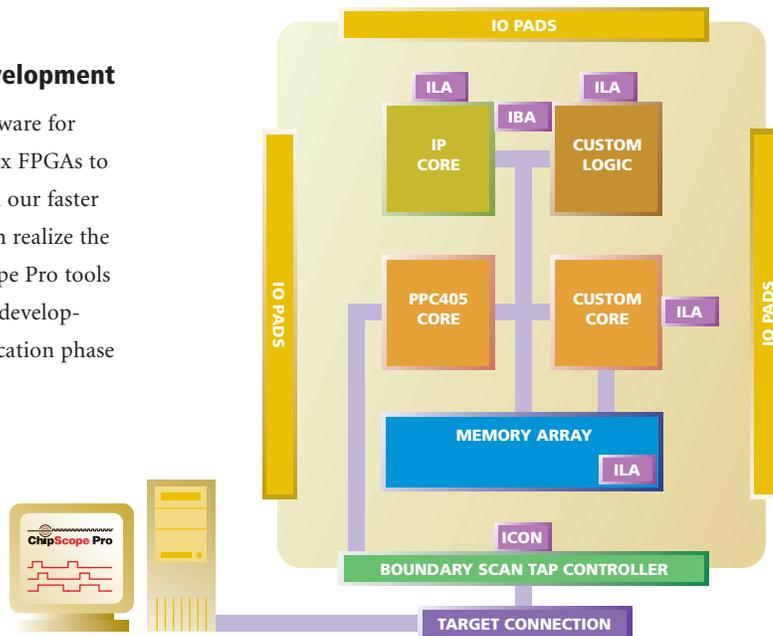
- **Faster Completion with On-chip Debugging** – Verify and debug your design in hardware where things happen in real-time under real world conditions, at or near system operation speeds. Gain direct access to signals, nodes, and busses anywhere on the device, while it's on the board interacting with your system. No ASIC technology can match this capability, and no competing FPGA solution gives you this power.
- **Lower Project Costs** – A faster verification cycle saves you time and money. Getting it right in verification means you have less product failure and recall from the field, which lowers your project costs even further.
- **Full Operation with All Xilinx FPGAs** – ChipScope Pro tools work with all Xilinx FPGAs, including Virtex-II Pro™. You can debug both logic and embedded system designs with the same real-time debugging and verification system.
- **Ease-of-Use** – Simply insert the cores into your design and you're ready for debugging. ChipScope Pro works directly with our ISE design tools providing a complete, affordable solution that supports on-chip logic and bus analysis.
- **Logic Analysis** – ChipScope Pro includes a Logic Analyzer to help you debug your system performance and track down critical problems.
- **Deep External Sample Storage** – Through the Agilent Trace Core in ChipScope Pro, you can link to the Agilent FPGA Trace Port Analyzer (sold separately from Agilent), which provides over 2 Meg of sample storage without using any extra FPGA memory resources. In addition to providing deep sample storage this solution enables time stamps and remote debugging capability through an Ethernet port.

Xilinx Solutions Lower the Cost of System Development

Use our ChipScope Pro for logic verification, our ISE software for programmable systems design, and the entire line of Xilinx FPGAs to create the ideal design and verification platform. Through our faster design cycles and advanced debugging capabilities you can realize the lowest cost of logic development available today. ChipScope Pro tools are available now and can help you greatly reduce overall development time by shortening the critical debugging and verification phase of your design.



ChipScope Pro integrated logic analyzer



ChipScope Pro core insertion and debug

Take the Next Step

Visit www.xilinx.com/chipscopepro to get more detailed information on ChipScope Pro tools and the Agilent solution, and download our free 30-day evaluation version. ChipScope Pro tools are also available for purchase from your local sales office or from the Xilinx on-line store.



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