

For more info, visit <http://www.xilinx.com/products/xaw/coolvhdlq.htm>.

dcx.zip

7 Kb Uploaded: 05-23-2002

This application note provides a functional description of VHDL source code for a N x N Digital Crosspoint Switch. The code is designed with eight inputs and eight outputs in order to target the 128-macrocell CoolRunner-II™ CPLD device but can be easily expanded to target higher density devices. For All Platforms

Category: Documentation, App Note

demo_bd_cr2.zip

28 Kb Uploaded: 08-22-2002

VHDL files and schematics for the CoolRunner-II LCD demo board. Simulation file included. For All Platforms

SW Release: All

mb_simple.zip

10 Kb Uploaded: 11-10-2002

This Design Example demonstrate how to use EDK3.1.1 (EDK3.1 SP1) to create a simple MicroBlaze system.

Solution #: 16105

For Win 2000

SW Release: F5.11

Category: Documentation, Quick Start Guide

oscilloscope_ver1.0.zip

56 Kb Uploaded: 09-13-2001

Reference Design that implements an Oscilloscope on the Insight Springboard Development Platform. This .zip file contains VHDL source code as well as PocketC source code. The design is described in XAPP149

For All Platforms

SW Release: All

Category: Documentation, App Note

uart_verilog.zip

10 Kb Uploaded: 03-18-2002

This application note provides a functional description of VHDL and Verilog source code for a UART. The basic functions of a UART are a microprocessor interface, double buffering of transmitter data, frame generation, parity generation, parallel to serial conversion, double buffering of receiver data, parity checking, serial to parallel conversion.

For All Platforms

xapp146.zip

230 Kb Uploaded: 03-05-2002

This application note will discuss the design of an eight channel Digital Volt Meter for the Handspring Visor. Specifically, it will illustrate how to use a Xilinx ultra-low power CoolRunner CPLD to interface a Texas Instruments ADS7870 Data Acquisition System to the Handspring Springboard expansion slot.

For All Platforms

SW Release: All

[xapp147.zip](#)**42 Kb Uploaded: 03-05-2002**

This application note presents development aids to help designers successfully and easily create Handspring Springboard Module designs. It includes a general discussion of the overall process, a summary of available software tools and an introduction to Xilinx CoolRunner CPLDs as used on the Insight Electronics Springboard Development Card.

For All Platforms

SW Release: All

[xapp333.zip](#)**954 Kb Uploaded: 03-18-2002**

I2C design module with fixes to support post route simulation from within WebPACK 4.2WP0.0

For All Platforms

SW Release: F4.11

Category: Documentation, App Note

Dependencies: xapp333.pdf

[xapp345_vhdl.zip](#)**36 Kb Uploaded: 03-22-2002**

VHDL customer download files for XAPP345. XAPP345 details the design of an IrDA interface using a CoolRunner CPLD. Please see XAPP345 for more information.

For All Platforms

Category: Documentation, App Note

[xapp348.zip](#)**487 Kb Uploaded: 03-18-2002**

This application note details the VHDL implementation of Serial Peripheral Interface (SPI) master in a Xilinx CoolRunner CPLD. The SPI is a full-duplex, synchronous, serial data link that is standard across many microprocessors, microcontrollers, and peripherals. It enables communication between microprocessors and peripherals and/or interprocessor communication.

For All Platforms

[xapp349.zip](#)**29 Kb Uploaded: 03-18-2002**

This application note details the VHDL implementation of an 8051 microcontroller interface in a Xilinx CoolRunner CPLD. This design consists of a state machine that interprets the 8051 bus cycles to read and write data to a set of registers called a register file. Communication between the 8051 microcontroller and application logic is accomplished through this register file.

For All Platforms

[xapp353.zip](#)**1063 Kb Uploaded: 02-25-2002**

VHDL source files to accompany xapp333 application note. This includes all of the latest fixes.

For All Platforms

SW Release: All

[xapp355.zip](#)**33 Kb Uploaded: 09-18-2001**

Contains reference design that implements the serial interface for the Texas Instruments ADS7870 Data Acquisition System. Can easily be customized for your own design!

For All Platforms

SW Release: All

Category: Documentation, App Note

[xapp356.zip](#)**341 Kb Uploaded: 01-29-2002**

This application note illustrates the implementation of a Handspring™ Springboard™ module design. The XPATH (Xilinx Pressure Altimeter Temperature Heading) module described here is implemented using the Xilinx CoolRunner™ CPLD and Handspring™ Visor. CoolRunner CPLDs are the lowest power CPLD available and the ideal target device for portable handheld applications. This application note describes

For All Platforms

SW Release: All

Category: Documentation, App Note

[xapp357.zip](#)**49 Kb Uploaded: 09-18-2001**

Simple reference design that blinks the LED's on the Insight Development Board. This illustrates how the CoolRunner CPLD can be used to decode the Springboard™ address and data lines.

For All Platforms

SW Release: All

Category: Documentation, App Note

[xapp363.zip](#)**102 Kb Uploaded: 10-18-2001**

Design files for XAPP363. Sonic Access Module Handspring Design. Submission for "Cool Module Design Contest". Refer to XAPP363 for more information.

For All Platforms

Category: Documentation, App Note

[xapp364.zip](#)**3257 Kb Uploaded: 10-25-2001**

Design files for XAPP364: Handheld Sound Bouncer. These were the design files used in the Sound Bouncer submission for the Cool Module Design Contest

For All Platforms

SW Release: All

[xapp365.zip](#)**58 Kb Uploaded: 10-29-2001**

Automotive Scan Tool Design files as described in XAPP365. This was a Cool Module Design Contest Finalist submission

For All Platforms

SW Release: All

[xapp366.zip](#)**110 Kb Uploaded: 11-08-2001**

Source Files for XAPP366: Handheld Musical Instrument Tuner. These are the design files used by the Musical Instrument Tuner design submitted for the Cool Module Design Contest. It was implemented using the Insight Springboard Development Kit.

For All Platforms

SW Release: All

[xapp367.zip](#)**1945 Kb Uploaded: 11-30-2001**

Download pack for XAPP367. Handspring CMDC design entry, Chatterbox. Please see XAPP367 for more information.

For All Platforms

Category: Documentation, App Note

[xapp368.zip](#)**540 Kb Uploaded: 11-30-2001**

XAPP368.zip contains the design files for the Handheld Logic Analyzer submission for the Cool Module Design Contest. These design files are described in Xilinx Application Note, XAPP368.

For All Platforms

[xapp369.zip](#)**307 Kb Uploaded: 12-05-2001**

Source files for XAPP369: Handheld 1553 Bus Analyzer. This zip file contains the VHDL and PocketC source files for 1553 Bus submission in the Cool Module Design Contest

For All Platforms

SW Release: All

[xapp370.zip](#)**566 Kb Uploaded: 12-06-2001**

XAPP370.zip contains the source files for the Grand Prize Winner of the Cool Module Design Contest, "Cool-Trak". This zip file contains the VHDL and C source code for the project. They are described in Xilinx Application Note XAPP370.

For All Platforms

SW Release: All

[xapp378.zip](#)**146 Kb Uploaded: 06-20-2002**

Customer pack for XAPP378. Design examples for implementation of CoolRunner-II features: DualEDGE registers, clock divider, CoolCLOCK, and DataGATE. Please refer to XAPP378 for more information.

For All Platforms

Category: Documentation, App Note

[xapp383.zip](#)**35 Kb Uploaded: 06-24-2002**

Customer download pack for XAPP383. Single Error Correction, Double Error Detection (SECDED) using a CoolRunner-II CPLD. For more information, refer to XAPP383.

For All Platforms

Category: Documentation, App Note

[xapp387.zip](#)**709 Kb Uploaded: 12-16-2002**

This application note describes the implementation of an 8-bit microcontroller design using a CoolRunner™-II CPLD. The PicoBlaze Microcontoller instructions can be customized to make an application-specific microcontroller. It is also possible to integrate the microcontroller and its program memory into a single CoolRunner-II device, eliminating the need for an external memory.

For All Platforms

SW Release: F5.11

Category: Documentation, App Note

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