

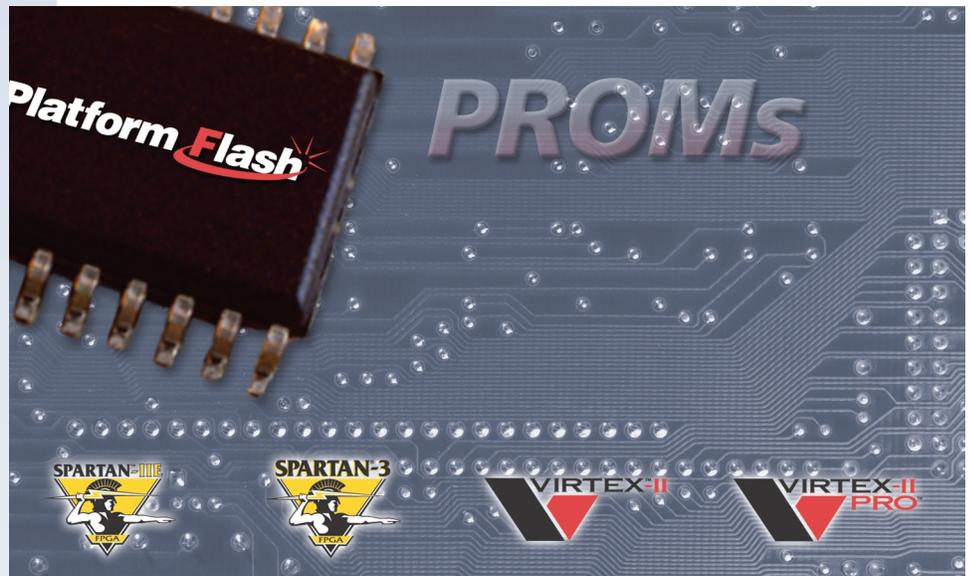


# Platform Flash PROM

## Industry's Lowest Cost Configuration Solution

You develop your design, route it with Xilinx ISE tools, and choose a PROM for configuration. But which PROM? Do you use an in-system-programmable device that costs more, or, a one-time-programmable device that costs less but can't be reprogrammed? Do you choose a PROM meant for a different FPGA because it is readily available and minimizes your manufacturing inventory?

The Xilinx Platform Flash PROM family solves all of these problems. In a single family that configures all Xilinx FPGAs, you get the lowest cost per megabit of any FPGA configuration PROM – and a very small footprint device. Best of all, you get in-system programmability that gives you the flexibility critical to successful prototyping, system testing, and production. Xilinx Platform Flash PROMs are the right solution for your FPGA design.



### In-System Programmability At the Lowest Cost Per Megabit

Lower your production costs with a single, flexible, in-system programmable PROM family. Xilinx Platform Flash PROMs give you a complete solution for all your FPGA configuration needs:

- **Lowest cost per megabit configuration PROM**
- **Smallest area per megabit**
  - Reduces configuration board space
  - VO20 and the FS48 packages
- **One 1-to-32 Mbit PROM family**
  - Use just one Platform Flash PROM to configure any Xilinx FPGA
  - Simplifies manufacturing flow
  - Reduces inventory cost
- **In-system programmability**
  - Makes design changes easy during development and verification.
  - Simplifies manufacturing flow and board test by supporting on-board programming.
  - Enables easy field upgrades.
- **Xilinx advanced compression technology**
  - Available in high-density devices
  - Increases effective PROM density
  - Supports advanced compression technology that provides for storage of up to 50% more bits, allowing the use of a smaller density, lower cost PROM.
  - Xilinx ISE tools and programming support
  - Use existing tools with the Platform Flash PROM family to lower your configuration costs today.

## Unrivaled Density Range and Freedom

The Platform Flash PROM family has six members: Three low-density devices and three high-density devices. Together they offer a wide range of densities to support configuration of all Xilinx FPGAs:

- **Low-Density Devices** (XCFxxS)
  - Serial configuration PROMs in densities of 1-, 2-, and 4-Mbits in the VO20 package.
- **High-Density Devices** (XCFxxP)
  - 8-, 16-, and 32-Mbits densities in FS48 package (Thin Flat Ball Grid Array)
  - Both serial and parallel configuration.

Using the Platform Flash family of configuration PROMs gives you these advantages over competing products:

- Up to 32-Mbits of configuration space in a small (72 sq-mm) footprint allows you to store several programs in a single, very small PROM. Use this device for even the largest FPGA densities, or, use one PROM to store multiple FPGA bitstreams for daisy-chain configuration.

- Take advantage of Design Revisioning, by programming one high-density PROM with multiple designs. Then use the same device and the same board for multiple applications.
- The Xilinx Advanced Compression Technology provides for storage of up to 50% more bits, reducing your cost further.
- Platform Flash PROMs comply with the industry standard IEEE 1532 interface so you can use existing equipment to test and program devices. No need for additional programming expense.

### Take the Next Step

Visit our website for more information or call your local sales office or distributor for a presentation and software tool demonstration.

[www.xilinx.com/products/platformflash](http://www.xilinx.com/products/platformflash)

	XCF01S	XCF02S	XCF04S	XCF08P	XCF16P	XCF32P
<b>Density</b>	1Mb	2Mb	4Mb	8Mb	16Mb	32Mb
<b>JTAG Prog</b>	√	√	√	√	√	√
<b>Serial Configuration</b>	√	√	√	√	√	√
<b>SelectMAP Configuration</b>				√	√	√
<b>Compression</b>				√	√	√
<b>VCC (V)</b>	3.3	3.3	3.3	1.8	1.8	1.8
<b>VCCO (V)</b>	1.8 – 3.3	1.8 – 3.3	1.8 – 3.3	1.5 – 3.3	1.5 – 3.3	1.5 – 3.3
<b>VCCJ (V)</b>	1.8 – 3.3	1.8 – 3.3	1.8 – 3.3	1.5 – 3.3	1.5 – 3.3	1.5 – 3.3
<b>Configuration Clock (MHz)</b>	33	33	33	50	50	50
<b>Package</b>	VO20	VO20	VO20	FS48	FS48	FS48

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