

## Xilinx Record-Breaking Technology – Today

by CARLIS COLLINS ◆ Editor

We are aggressively expanding the limits of both device and software technology, as you will see from the articles in this

issue of *XCell*. We are the number one supplier of FPGAs worldwide because we spend a large portion of our revenues on research and development. As a result, we have created the industry's most advanced, highest-performance, 0.25-micron technology, and the broadest

line of powerful and efficient device architectures that are creating new design opportunities never before possible. Our new device families include:

- Our XC4000XV family of high-performance FPGAs (up to 500,000 system gates at over 100 MHz).
- ➤ Our Virtex<sup>™</sup> family of high-performance, very dense, system-level FPGAs (up to 1,000,000 system gates at over 100 MHz).
- ➤ Our Spartan<sup>™</sup> family of low-cost devices (up to 40,000 system gates at over 80 MHz).
- ➤ Our XH3 family of HardWire<sup>TM</sup> ASIC devices that provide unprecedented ease-ofuse, quick turnaround, and guaranteed performance for high-volume applications.

## **Powerful Software**

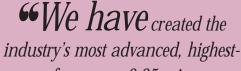
We have also been working diligently to create the most powerful and efficient

development tools in the industry. With our Alliance Series and Foundation Series software, you can quickly develop and debug FPGA and CPLD designs for all of our devices, using any combination of design methods, including VHDL and schematics. These development tools give you the most comprehensive and powerful solutions available anywhere, and we are constantly refining and updating our tools with the latest software advances, both from our in-house development team and from our Alliance partners.

Along with our advanced development tools, we offer an expanding library of intellectual property cores that will become increasingly important as you begin to create designs for our high-density devices. With the use of intellectual property, your next generation designs will be faster and easier to produce. In addition, we continue to develop new tools that will automatically create complex, customized functions for you. For example, using our core generator tools, you will soon be able to automatically create massively parallel DSP functions that are much faster than any commercially available DSP device.

We are doing our best to anticipate your needs and provide you with the most advanced technology, services, and technical support. In this magazine, and on our WebLINX Web site (www.xilinx.com), we intend to provide you with interesting and informative articles and timely reference information that make your job easier and more productive.

We welcome and respond to your requests and your suggestions for improvement in all areas. Please let us know how we're doing by writing to editor@xilinx.com. ◆



performance, 0.25 micron technology, and the broadest line of powerful and efficient device architectures that are creating new design opportunities never before possible.\*

## **XCell**

Xilinx, Inc.

2100 Logic Drive San Jose, CA 95124-3450

**Phone:** 408-559-7778 **FAX:** 408-879-4780

©1998 Xilinx Inc. All rights reserved.

XCell is published quarterly for customers of Xilinx, Inc. XILINX and the Xilinx logo are registered trademarks of Xilinx, Inc. Spartan, Virtex, HardWire, Alliance Series, Foundation Series, AllianceCORE, LogiCORE, WebLINX, SelectRAM, SelectRAM+, Dual Block, FastFLASH, and all XC-prefix products are trademarks, and "The Programmable Logic Company" is a service mark of Xilinx, Inc. Other brand or product names are trademarks or registered trademarks of their respective

