
XC5200 Family Breaks The \$4 Price Barrier

FPGA benefits now available to consumer and high-volume markets

Xilinx shipped the millionth unit of the XC5200 FPGA family in the fourth quarter of 1996. The XC5200 family reached this milestone faster than any previous FPGA product family.

As one result of the migration of this family to 0.5 μ three-layer-metal process technology, Xilinx has significantly reduced component die sizes, leading to dramatically reduced prices. In fact, Xilinx has introduced the world's first FPGA for less than \$4. The direct price for the 256-logic-cell XC5202-6PC84C is now \$3.95 (in 25,000 piece quantities).

The migration of the family to the 0.5 μ process has resulted in a "pad-limited" die size; that is, a die size limited by the number of I/O on the device, not the amount of logic. This, in turn, leads to production cost parity with low-end gate arrays. As a result, high-volume products can now achieve their cost and margin targets while obtaining all the

traditional benefits of FPGA technology, including design flexibility and off-the-shelf availability. The XC5200 family is now the ideal logic solution for consumer and other high-volume products that require both low cost and fast time-to-market, such as set-top boxes, PC add-in cards, cable modems, and digital cameras.

With five XC5200 family devices ranging from 2,000 to 23,000 maximum logic gates in 15 different packages, there are now over 100 different device/package/speed combinations available to meet your exact design requirements, all at very affordable prices. ♦

