Xilinx introduces-
New High-Speed Download Cable

MultiLinx reduces download times by 10X over the older XChecker solution.

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Our new download cable, called MultiLINX™, features a high-speed USB input (or RS232 if you prefer) and a full set of interfaces for the SelectMAP (Virtex), Slave Serial, and IEEE JTAG Download Modes Using a USB cable (with an 8-Mbit per second throughput) the cable can download an XCV1000 in less than a second compared to more than 14 minutes using the older XChecker Cable.

MultiLINX supports all Xilinx FPGAs and CPLDs (Virtex, XC4000XL/XLA/XV, XC5000, Spartan/XL, XC9500). It automatically switches to a wide range of interface voltages including TTL (5 volts), LVTTL (3.3 volts), LVCMOS (3 volts), JESD8-7 (2.5 volts normal and 2.5 volt wide), without the need for a separate external adapter. It's also compatible with 1.8-volt devices (with VCCIO set to 2.5 volts), and it

MultiLinx Download Cable

accepts any voltage from 3.0 to 5.0 volts as a power supply input (from either a target application or external power supply).

Download speeds are increased by 50% even when using the standard 25-pin RS232 cable with the MultiLINX on-board bitstream compression.

MultiLINX offers both flexibility and high-performance to make downloading and programming Xilinx programmable logic easier and faster than ever. ⚡

For more information see:
http://www.xilinx.com/support/programr/cables.htm#Multilinx