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FOR IMMEDIATE RELEASE

XILINX AND PARTNERS TO DEMONSTRATE INTERNET-BASED DESIGN AT DAC

Remote teams collaborate on complex designs using Internet Team Design (ITD) technology

SAN JOSE, Calif., May 17, 1999—Continuing with its Silicon Xpresso initiative, Xilinx, Inc., (NASDAQ:XLNX) today announced its Alliance partners will show their support for its Internet Team Design (ITD) technology with real-time demonstrations at the Design Automation Conference (DAC) in June.

ITD technology leverages Java language to combine Xilinx design implementation tools and the Internet for remote design teams to work together effectively on today's large, complex FPGA designs. As part of the demonstration, users will access the ITD web site from the Alliance partner booths using a web browser. For the first time, designers will be able to utilize the power of this technology by creating design modules using any entry method at the Alliance Partner booths and sending them over the Internet to the Xilinx booth where results will be compiled and the design completed. Demonstrations will be available at Xilinx booth (#2532), Exemplar booth (#1842), Synopsys booth (#1215) and Synplicity booth (#2032).

Xilinx is the first programmable logic vendor to provide this infrastructure for organizing and coordinating multiple modules, integrating a variety of files, and linking multiple design projects that have been created by design teams and individuals working in different locales. ITD technology differs from other team-based design environments in its platform independence and use of standard web browsers as a design interface, eliminating a learning curve.

The size and use of programmable logic has been expanding rapidly and creating new challenges for design teams. These challenges include large and complex designs, multiple design inputs, from schematic to HDL, and remote locations of the designers. ITD technology addresses these challenges through internet-based communication; multiple input integration (including VHDL, Verilog, schematics and cores); and design coordination through an intelligent Design Control System (DCS).

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"By using the Design Control System in the ITD solution, the project leader can work with a wide variety of engineers and design inputs in various locations," said Rich Sevcik, Xilinx senior vice president of software, cores and support. "ITD technology provides the means to efficiently and effectively integrate and coordinate the design inputs on an iterative basis. Designers will see how the project leader can use ITD technology and the Internet today to manage large design teams through complex projects for creating next-generation applications."

Exemplar Logic, a wholly owned subsidiary of Mentor Graphics Corporation, supports Xilinx Virtex FPGAs in its advanced synthesis tool, LeonardoSpectrum. LeonardoSpectrum is a synthesis environment designed to handle the largest and most complex FPGA devices. The gate array market segment, a focus of both Exemplar Logic and Xilinx, demands team-based design and Internet-based tools. "ITD technology meets the challenges of team design," said Tom Feist, vice president of marketing for Exemplar Logic. "It is a natural extension of LeonardoSpectrum's team design capabilities. Exemplar's robust support for bottom-up and incremental design, combined with Xilinx ITD communication and design integration capabilities help to create an effective working environment for dispersed design teams."

"The Internet eases the communications burden for engineering teams collaborating with designers and partners working on the same project from different locations," according to Jay Michlin, vice president and general manager of the FPGA business unit at Synopsys. "This is particularly important for large devices in which teams are already splitting projects among remote sites which use Synopsys' world-class synthesis and verification tools to design each module. The Xilinx introduction of its Internet Team Design technology will allow designers to leverage the Internet to deliver modules around the world, and then complete their design in the chosen location."

Andy Haines, vice president of marketing for Synplicity Inc. is excited to be working with Xilinx on the implementation of the ITD methodology. "Maximizing designer productivity is of prime importance to Synplicity. Xilinx's use of the Internet to join our tools with theirs is an out-of-the-box approach to increasing productivity. This is another example of the close working relationship between

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Synplicity and Xilinx, just as is the superior quality of results from our Synplify synthesis tool when targeting the Virtex architecture."

Xilinx is the leading innovator of complete programmable logic solutions, including advanced integrated circuits, software design tools, predefined system functions delivered as cores, and unparalleled field engineering support. Founded in 1984 and headquartered in San Jose, Calif., Xilinx invented the field programmable gate array (FPGA) and commands more than half of the world market for these devices today. Xilinx solutions enable customers to reduce significantly the time required to develop products for the computer, peripheral, telecommunications, networking, industrial control, instrumentation, high-reliability/military, and consumer markets. For more information, visit the Xilinx web site at www.xilinx.com.

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