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<u>FOR IMMEDIATE RELEASE</u>

XILINX SHIPPING LARGEST MEMBER OF ISP CPLD FAMILY

SAN JOSE, Calif., August 6, 1997—Xilinx, Inc., (NASDAQ:XLNX), today announced it has begun volume shipments of the newest member of the XC9500 family of aggressively-priced complex programmable logic devices (CPLDs) combining FLASH technology and an advanced architecture with in-system programming (ISP) capability. This largest member of the Xilinx CPLD family has 288 macrocells.

Five members now comprise the XC9500 CPLD family—the XC9536, XC9572, XC95108, XC95216 and the XC95288 devices—and range in density from 36 to 288 macrocells in a variety of packages. The XC9500 family features an architecture optimized for pin-locking. Pin-locking is a necessity for digital designers who want to take advantage of the XC9500 family's in-system programming (ISP) capability that enables easier prototyping, simpler manufacturing, and remote equipment upgrades.

"Our customer are responding to the pricing and features of the XC9500 family," said Evert Wolsheimer, vice president and general manager of the CPLD business unit. "Design wins are increasing at a high rate and we expect this to continue. As much as 30 percent of Xilinx's total design wins are derived from this product line. The company's long-term success will depend on its ability to deliver a broad range of products as customers choose a single supplier for their programmable logic needs."

Xilinx targets the innovative XC9500 family at ISP users, who constitute the fastest growing segment of the CPLD market. In the manufacturing process, for example, ISP permits the new Xilinx CPLDs to be programmed and tested as part of the production process. This eliminates the need for device programmers and helps prevent mechanical damage that can occur to CPLDs during the manual programming process. ISP also eliminates labeling, inventorying and assembling of unprogrammed parts

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or revising parts that already have been programmed. Moreover, in the area of product life-cycle management, the in-system programming capability of the XC9500 family allows manufacturers to design equipment that can be maintained or diagnosed remotely in the field, eliminating the need to physically change out parts.

Pricing, Availability, and Software Support

Pricing for the XC95288 device begins at $65.50 in 100-unit quantities of the HQ208 package. High-volume quantity pricing is projected to be $1.50 by the end of 1998 for the XC9536 PC44 package. A variety of software is available to support the new XC9500 devices through Xilinx's easy-to-use Foundation tools and third-party packages from members of the Xilinx Alliance Partner Program. Prices for the software packages begin at $495 for complete XC9500 support.

Device	XC9536	XC9572	XC95108	XC95144	XC95216	NEW XC95288
Macrocells	36	72	108	144	216	288
T_{PD} (ns)	5	7.5	7.5	7.5	10	15
Maximum User I/O	34	72	108	133	166	192
Availability	*NOW*	*NOW*	*NOW*	1Q98	*NOW*	*NOW*
Packages	44VQ 44PC	44PC 84PC 100TQ 100PQ	84PC 100TQ 100PQ 160PQ	100PQ 160PQ	160PQ 208HQ 352BG	208HQ 352BG

Founded in 1984, Xilinx is the world's largest supplier of programmable logic solutions comprising industry leading device architectures and world class design software. Headquartered in San Jose, Calif., the company pioneered the market for field programmable gate array (FPGA) semiconductor devices that provide high integration and quick time-to-market for electronic equipment manufacturers in the computer, peripherals, telecommunications, networking, industrial control, instrumentation, consumer electronics and high reliability/military markets. For more information on Xilinx, access the World Wide Web site at http://www.xilinx.com.

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