FOR IMMEDIATE RELEASE

Exemplar Logic announces support for Xilinx Virtex Series FPGAs

Fremont, California – October 26, 1998 – Exemplar Logic, the world leader in FPGA synthesis today announced the immediate support for Xilinx Virtex Series FPGAs in LeonardoSpectrum.

LeonardoSpectrum is a synthesis environment designed to handle the industries largest and most complex FPGA devices such as Virtex, which now offers over 1 million system gates of logic. "Virtex will have a profound effect on present HDL design methodologies," says Ellis Smith, CEO of Exemplar Logic. "Previous devices are mostly designed by a single engineer using a simple top-down approach. Virtex's immense gate counts will result in multi-engineer FPGA design teams using more sophisticated team and bottom-up design techniques." LeonardoSpectrum fully supports these large device design methodologies by giving users the power to group, flatten, constrain, optimize and assemble on a hierarchical block by block basis. In addition to high control, Exemplar Logic's FPGA Architecture Specific Technology (F.A.S.T.) optimization technology provides tremendous throughput and quality of results easily capable of handling Virtex's' high gate counts.

Both Exemplar Logic and Xilinx are focusing on the gate array market segment. "This is clearly a device designed to attract the gate array designer into the world of FPGAs," says Tom Feist vice president of marketing for Exemplar Logic. Volume pricing for the XCV50 device matches gate array pricing up to 50,000 gates and isn't that far off at 100K gates. If these parts can handle the design they are going to be tough to beat and with up to 660 user I/Os and clock frequencies exceeding 100 MHz they can handle a lot of designs. Add to that the reduced risk associated with FPGA devices and you've got a win/win solution!"

Exemplar has experienced a sharp increase in interest in its ASIC synthesis products over the past six months. According to Rich Sevcik, vice president of software and cores at Xilinx, engineers increasingly want the option to choose between ASICs and FPGAs later in the design process which is driving the need for a synthesis environment that does both. LeonardoSpectrum is the only true seamless ASIC and FPGA synthesis tool on the market today offering the ease of use and run times FPGA designers have come to expect plus the power and control demanded by ASIC designers on both UNIX and PC platforms.

About Exemplar Logic

Exemplar Logic, Inc. pioneered applying logic synthesis techniques to the design of FPGA, CPLDs, and ASICs. The company develops and markets EDA software tools, and is the world's number one leading PLD synthesis tool supplier, as reported by Dataquest (April 1997). Exemplar's products are sold and supported worldwide by Exemplar Logic, its OEMs and VARs, Mentor Graphics, and through the FPGA and CPLD component distribution channel.

Exemplar's design environments implement a complete high-level design (HLD) solution for FPGA, CPLD and ASIC design, offering synthesis, simulation and timing analysis for Windows 95, Windows-NT, and UNIX (HP, Sun) platforms in server and non-server environments.

Exemplar Logic is a privately held California corporation with headquarters located 6503 Dumbarton Circle, Fremont, CA 94555. Additional offices located throughout the world: Tokyo, London, Boston, and Denver. For information on Exemplar Logic's products, contact Exemplar Logic by calling 510-789-3333 or by sending eMail to sales@exemplar.com, or visit the Exemplar web site at www.exemplar.com

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