

**Press Contacts:**

Jeff Garrison  
Synplicity, Inc.  
(408) 548-6031  
jeff@synplicity.com

Lisa Neitzel  
Tsantes & Associates  
(408) 369-1500  
lisa@tsantes.com

**HOLD FOR RELEASE UNTIL OCTOBER 26**

**SYNPLICITY ADDS ENHANCED SUPPORT FOR VIRTEX;  
XILINX'S MILLION-GATE FPGAS**

**Synplify Speeds Time-to-Market For High-Density Virtex FPGA Designs**

SUNNYVALE, Calif., October 26, 1998—In conjunction with the shipping of Xilinx's (Nasdaq: XLNX) Virtex FPGAs, Synplicity®, Inc., a leading supplier of logic synthesis software for programmable logic design, today announced it has enhanced its Synplify® logic synthesis tool to further support this high-density family of FPGAs. Developed specifically for Xilinx's million-gate architecture, Synplify's innovative timing-driven technology mapper allows designers to quickly solve the complex development requirements of these high-performance FPGAs. With compile times that increase linearly with design size, rather than exponentially, Virtex users can employ Synplify to obtain high quality of results in a fraction of the time of traditional FPGA synthesis tools.

“Today, million-gate FPGAs are a reality, and to successfully design these devices synthesis tools are a requirement,” said Rich Sevcik, vice president of software for Xilinx. “This announcement represents another milestone in Xilinx and Synplicity's commitment to provide

(more)

leading-edge software support at the time of product delivery. We are pleased that Synplicity's robust tools will enable designers to take advantage of this increase in density."

As part of the formal alliance established between Synplicity and Xilinx earlier this year, Synplicity has delivered a powerful and highly integrated solution to Virtex designers. Synplicity's enhanced Virtex mapper in Synplify delivers higher performance by utilizing the advanced interconnect structure in Virtex for placement-based timing optimizations. By providing software support concurrent with Virtex availability, Synplicity reinforces its commitment to provide customers with timely, high-quality support of new programmable logic devices.

"As gate counts continue to rise and designs become more and more complex, it is imperative that chip architectures and EDA tools be developed concurrently," said Andy Haines, vice president of marketing for Synplicity. "Over the years, we have established an excellent relationship with Xilinx and, through frequent technical exchanges, have been able to implement innovative solutions that produce superior quality of results."

Haines continued, "We look forward to continuing this relationship and are committed to maintaining our technology leadership position in FPGA synthesis."

### **About Synplicity's Solutions**

Synplicity's offerings represent a new breed of synthesis for FPGA design engineers. The company's product line includes Synplify, a popular logic synthesis tool known for its quality results and speed; and HDL Analyst, a powerful HDL code analysis and debugging environment.

First introduced in 1995, Synplicity's Synplify synthesis tool represents a new breed of synthesis tools designed independent of existing academic or commercial code and features the company's innovative B.E.S.T.<sup>™</sup> algorithms. The tool accepts industry-standard Verilog and VHDL descriptions and produces optimized implementations for programmable devices from many leading vendors. The recent release of Synplify 5.0 contains a unique multi-level timing constraints management system, giving designers, for the first time, the most accurate automated solution combined with the most robust user-controlled features, providing designers flexibility in

(more)

the way they use their synthesis tool. Designed to deliver the highest quality of results, Synplify is also extremely fast and easy-to-use. It includes a built-in language-sensitive editor and optional graphical (block diagram) analysis tool that gives direct feedback for fast design debug.

### **Pricing and Availability**

Xilinx's Virtex FPGAs surpass the million-gate mark, run at over 100 MHz, and offer sufficient on-chip functionality to deliver the industry's first fully-programmable, system-level solution. Support for these devices is available now from Synplicity. Pricing for Synplify 5.0 node-locked Windows platform is \$12,000, and floating licenses for Windows or workstation platforms are \$24,000.

### **About Synplicity**

Founded in 1994, Synplicity, Inc., delivers the benefits of logic synthesis and embedded synthesis technologies to programmable logic designers by developing fast, easy-to-use, affordable tools with extremely high quality of results. Synplicity's electronic design automation (EDA) products support industry-standard design languages (VHDL and Verilog), run on popular platforms (Windows '95, Windows NT and UNIX), and support leading PLD manufacturers. The company is located at 610 Caribbean Drive, Sunnyvale, Calif. 94089. Telephone: 408/548-6000; Fax: 408/548-0050; Email: [info@synplicity.com](mailto:info@synplicity.com); Web: <http://www.synplicity.com>.

# # #

Synplicity, Synplify and B.E.S.T., are trademarks of Synplicity, Inc. All other brands or products are the trademarks or registered trademarks of their owners.